

1.1 Introduction

This section illustrates the steps to evaluate the performance of A/D and D/A converter on DCC(Data Conversion Card) based on the C5G.

1.2 System Requirements

The following items are required for C5G + DCC demonstration.

- DCC board x 1
- C5G Board x 1
- SMA Cable x 2

1.3 Setup the Demonstration

Figure 1-1 shows how to setup hardware for C5G _DCC demonstration

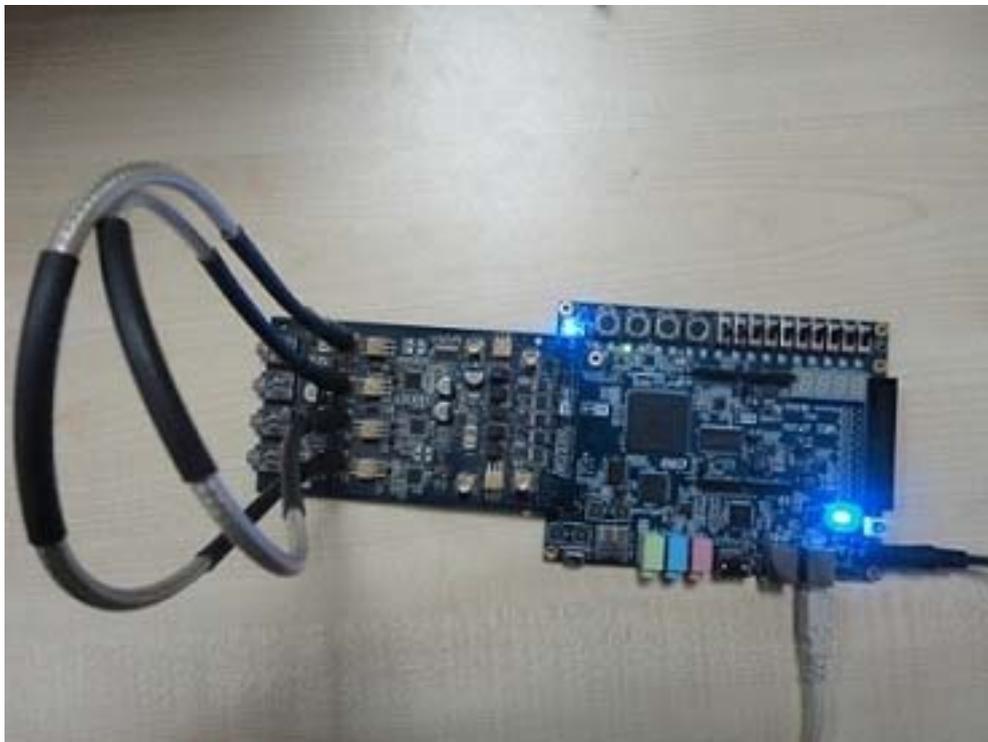


Figure 1-1 Connect DCC with C5G

1.4 Operation

■ Configuring the Board:

1. Ensure the connection is made correctly as shown in Figure 1-1
2. Use a SMA cable to connect DA-Channel A with AD-Channel A and use another SMA cable to connect DA-Channel B with AD-Channel B.
3. Use a USB cable to connect C5G with PC
4. Power-on C5G
5. Open \C5G_DCC\stp1.stp, as shown in Figure 1-2.

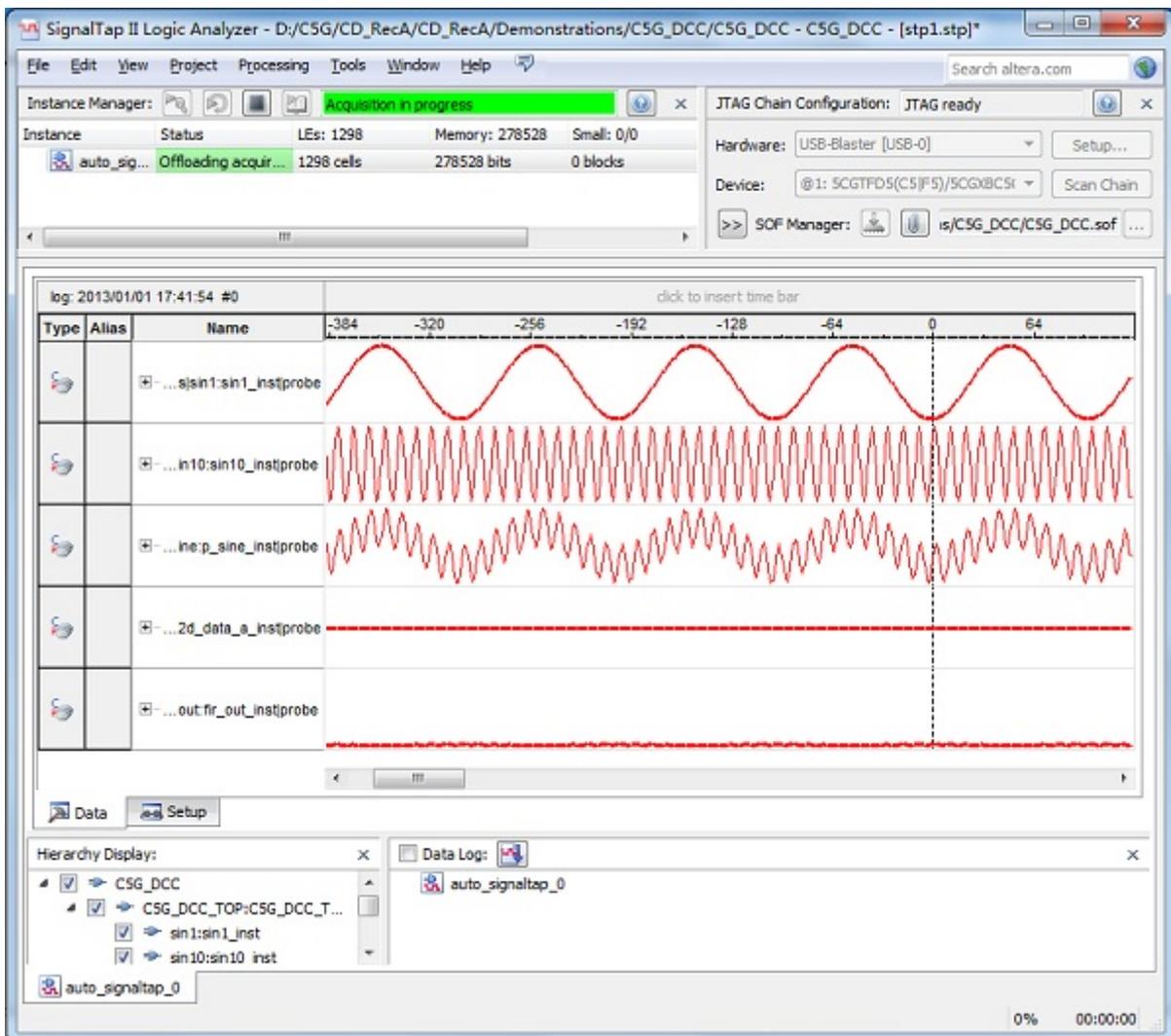


Figure 1-2 open signal tap II

■ Collecting Data Using the SignalTap II Logic Analyzer

1. Click “Program Device” after Hardware and Device are detected correctly.
2. Click “Run Analysis” and observe signals as Figure 1-2
3. Generate the SignalTap II List File as Figure 1-3

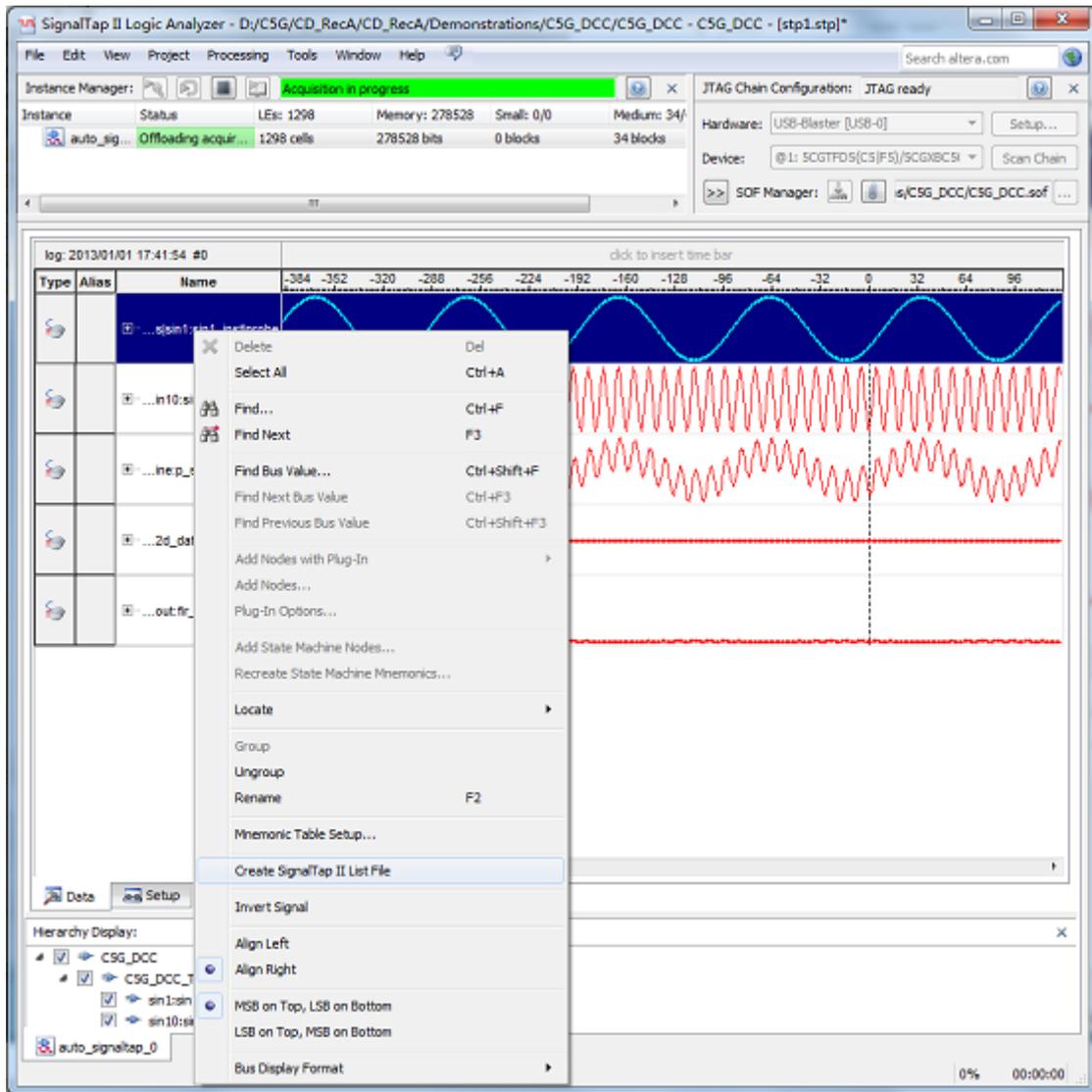


Figure 1-3 Using Quartus 13.0 sp1 SignalTap II to generate the SignalTap II List File