

NOTES:

1. Project Drawing Numbers:
- Raw PCB

100-0330617-B1
- Gerber Files

110-0330617-B1
- PCB Design Files

120-0330617-B1
- Assembly Drawing

130-0330617-B1
- Fab Drawing

140-0330617-B1
- Schematic Drawing

150-0330617-B1
- PCB Film

160-0330617-B1
- Bill of Materials

170-0330617-B1
- Schematic Design Files

180-0330617-B1
- Functional Specification

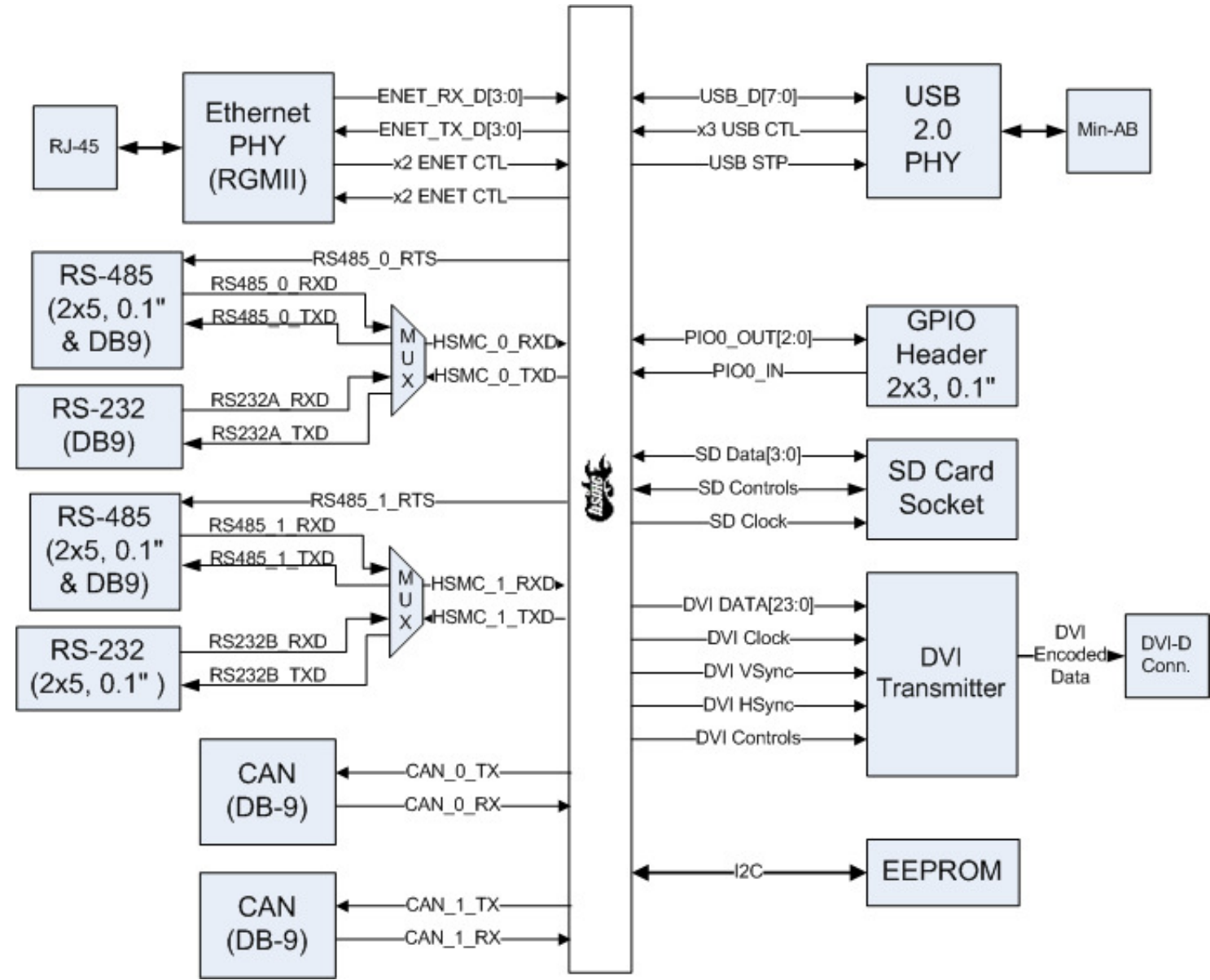
210-0330617-B1
- PCB Layout Guidelines

220-0330617-B1
- Assembly Rework

320-0330617-B1

2. 303 Parts, 32 Library Parts, 244 Nets, 1147 Pins

HSMC Communication Block Diagram

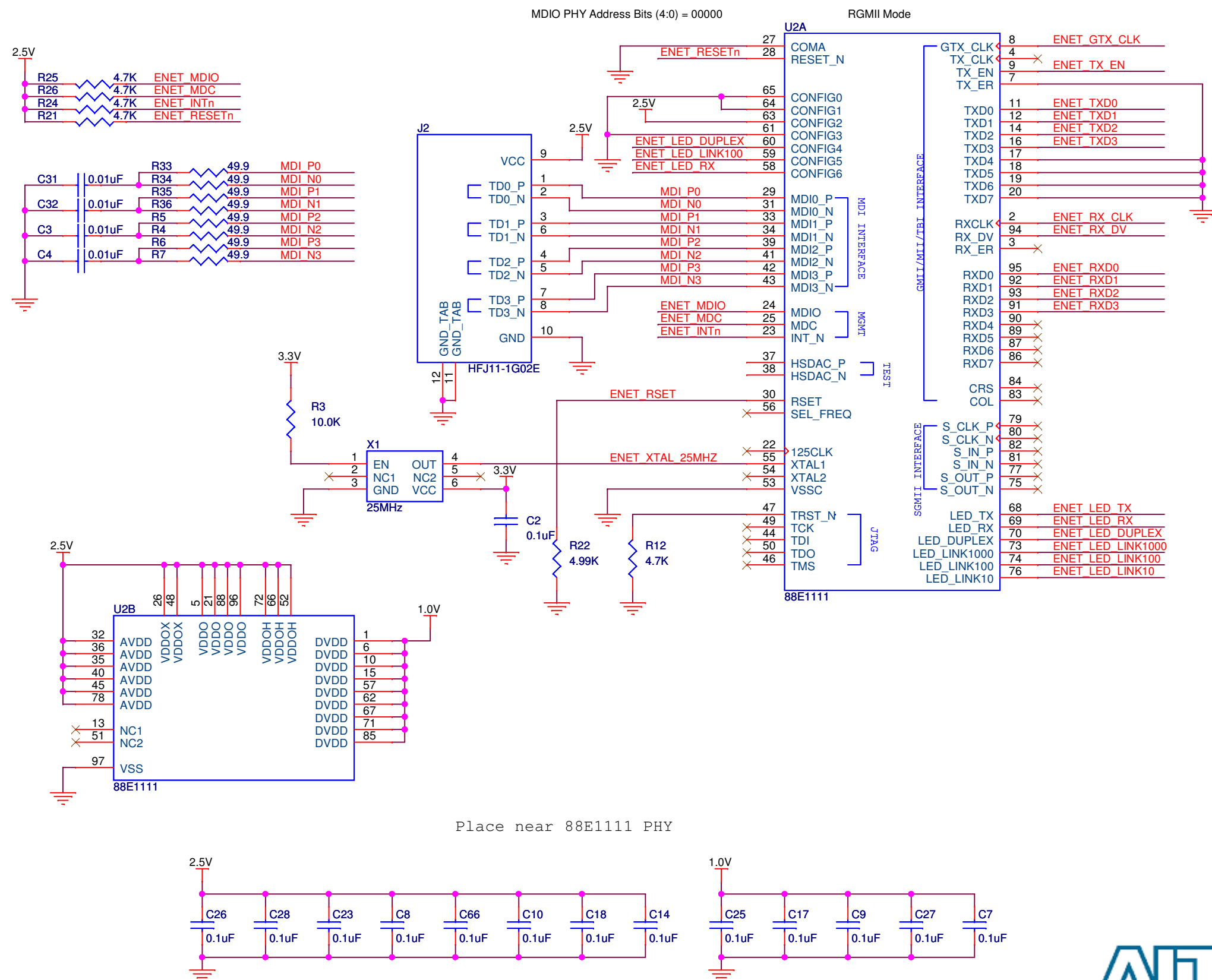


Digital Ground

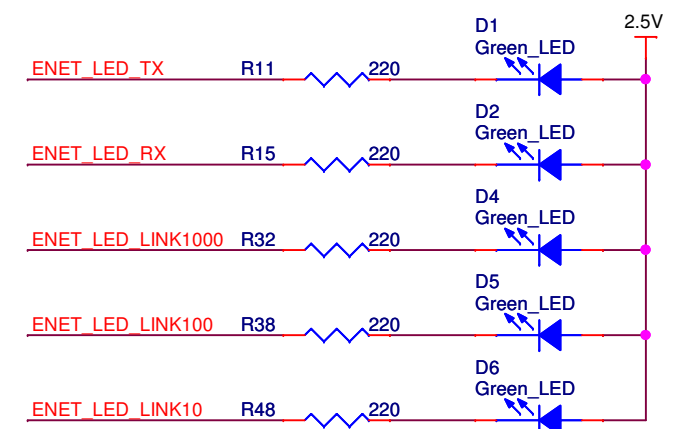
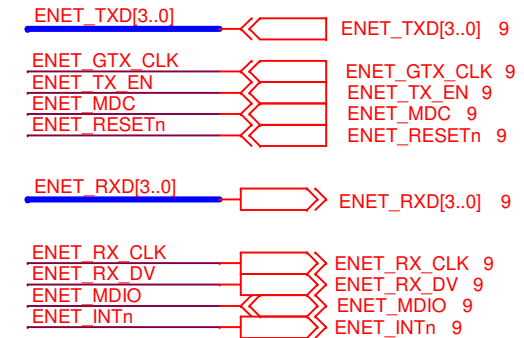


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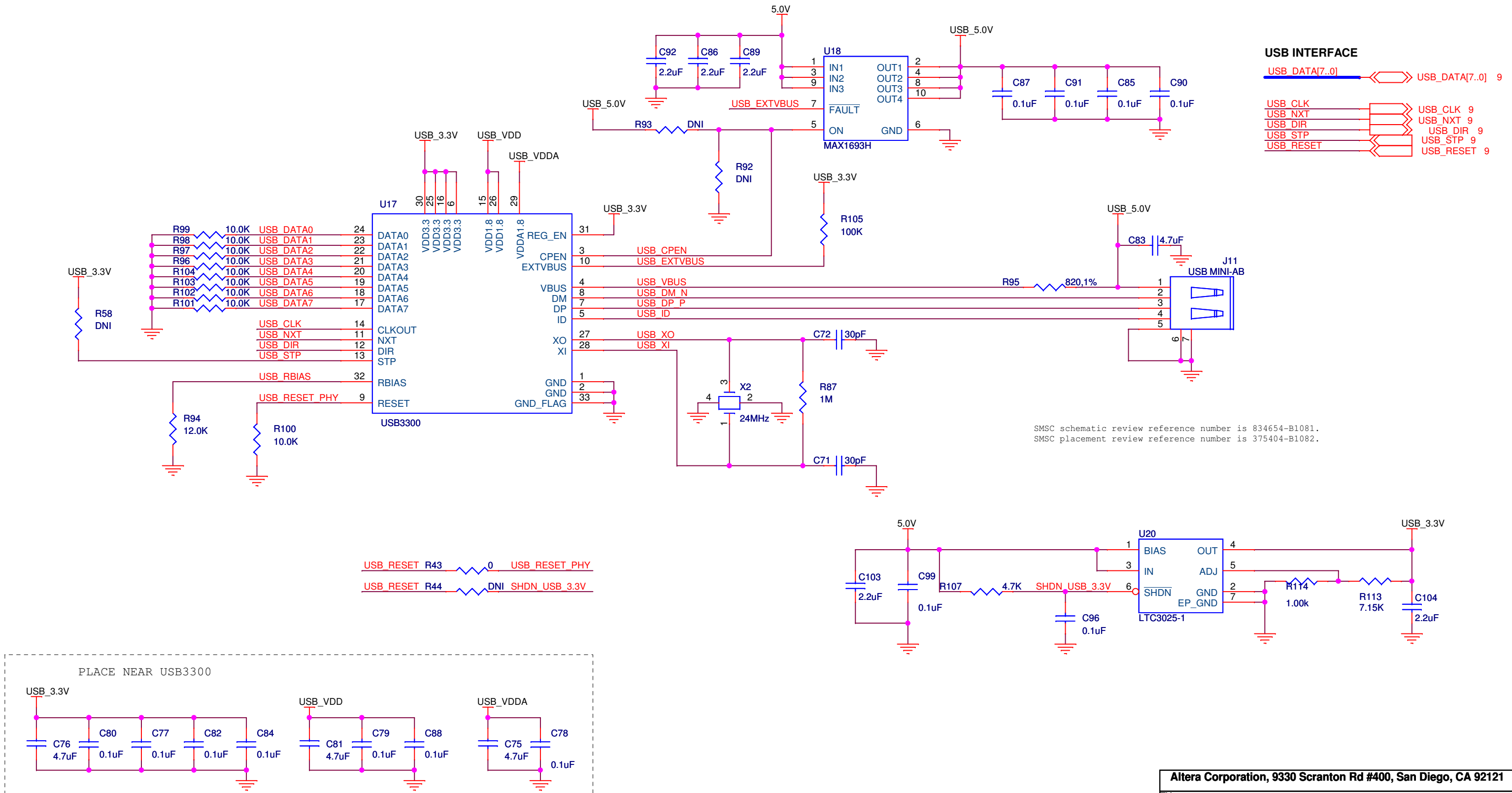
10/100/1000 Ethernet



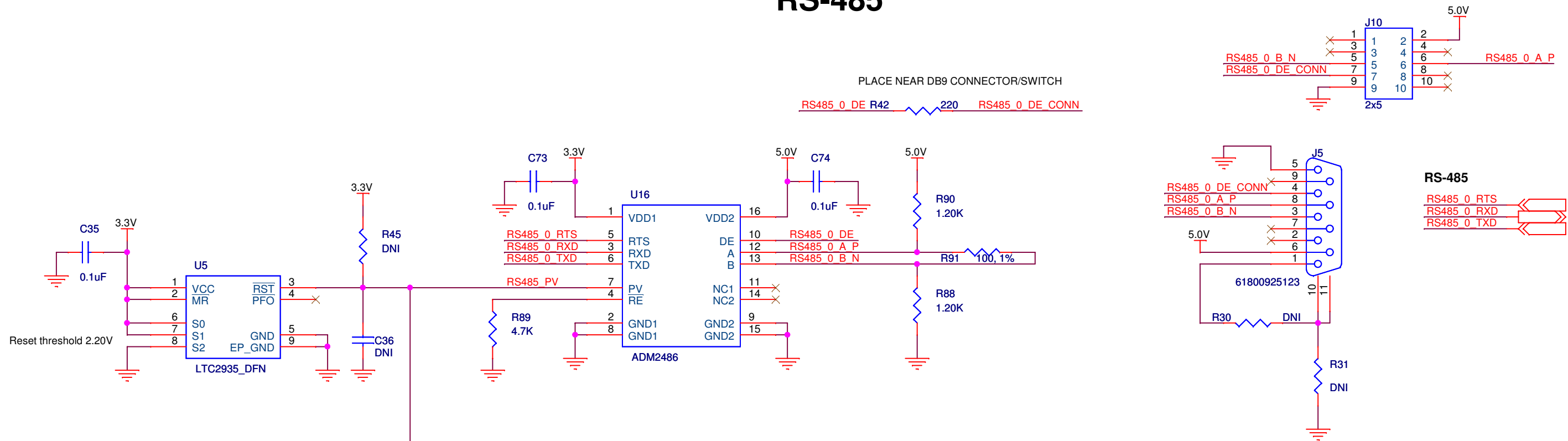
ETHERNET INTERFACE



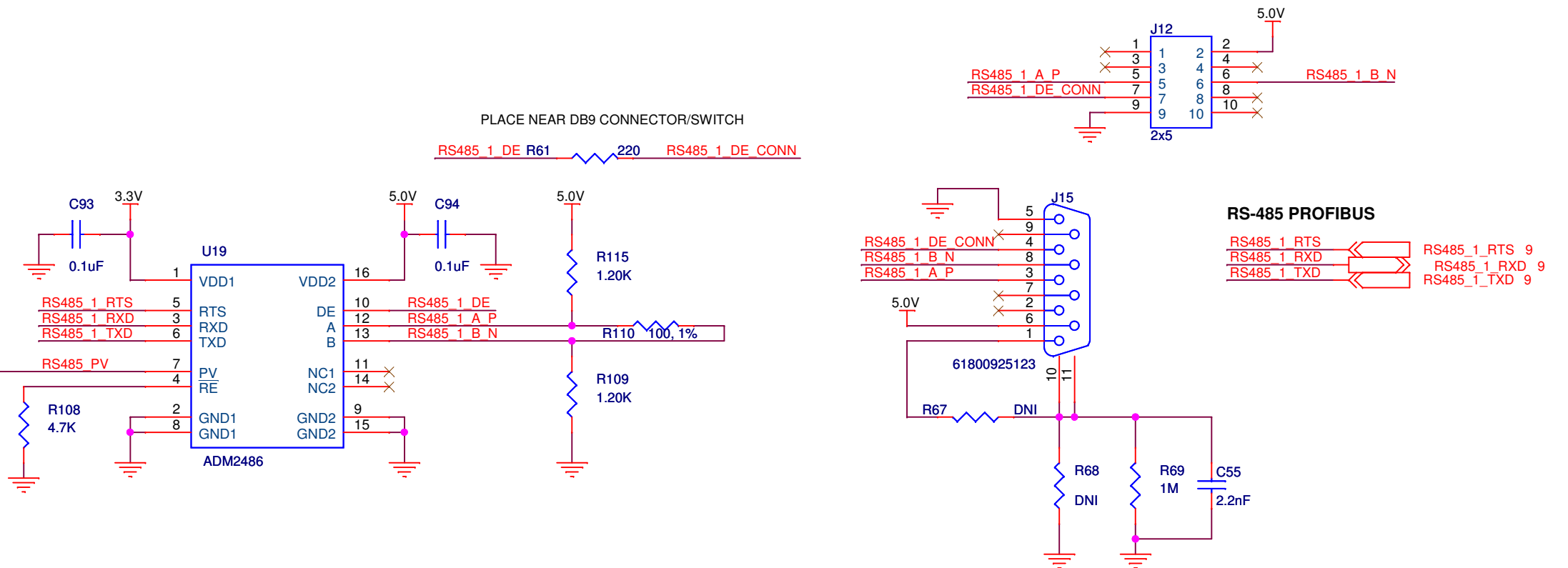
USB 2.0 OTG



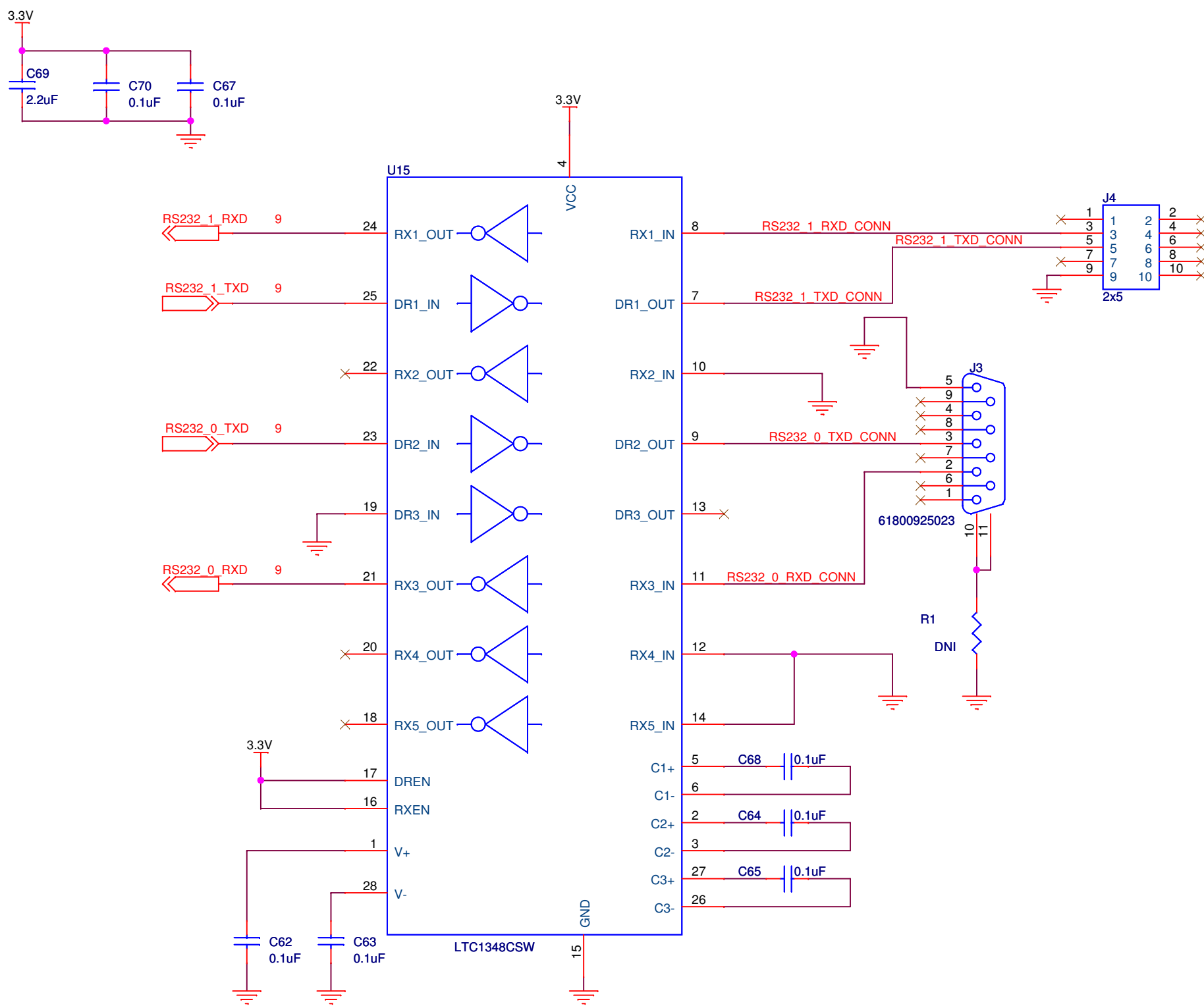
RS-485



Profibus (RS-485)

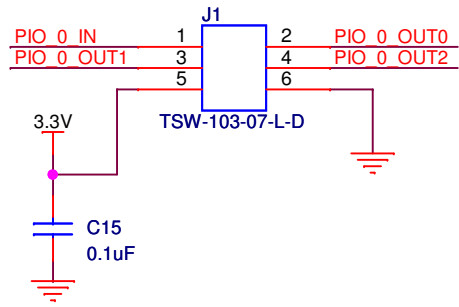
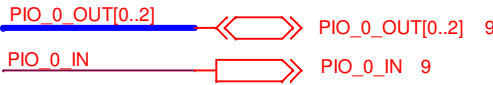


RS-232



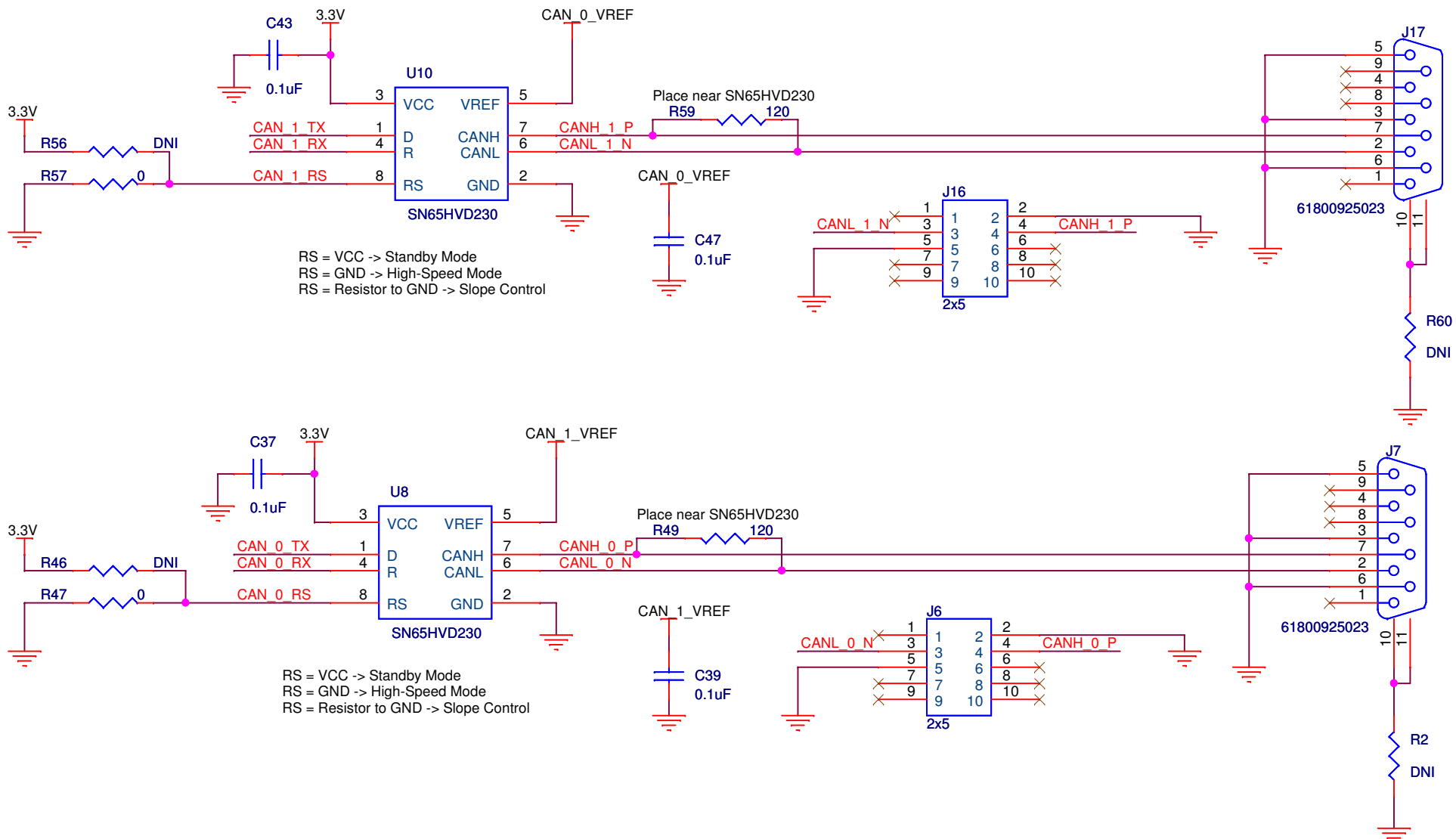
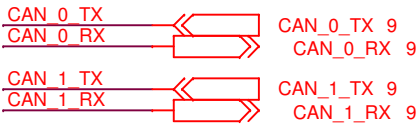
GPIO

GPIO INTERFACE



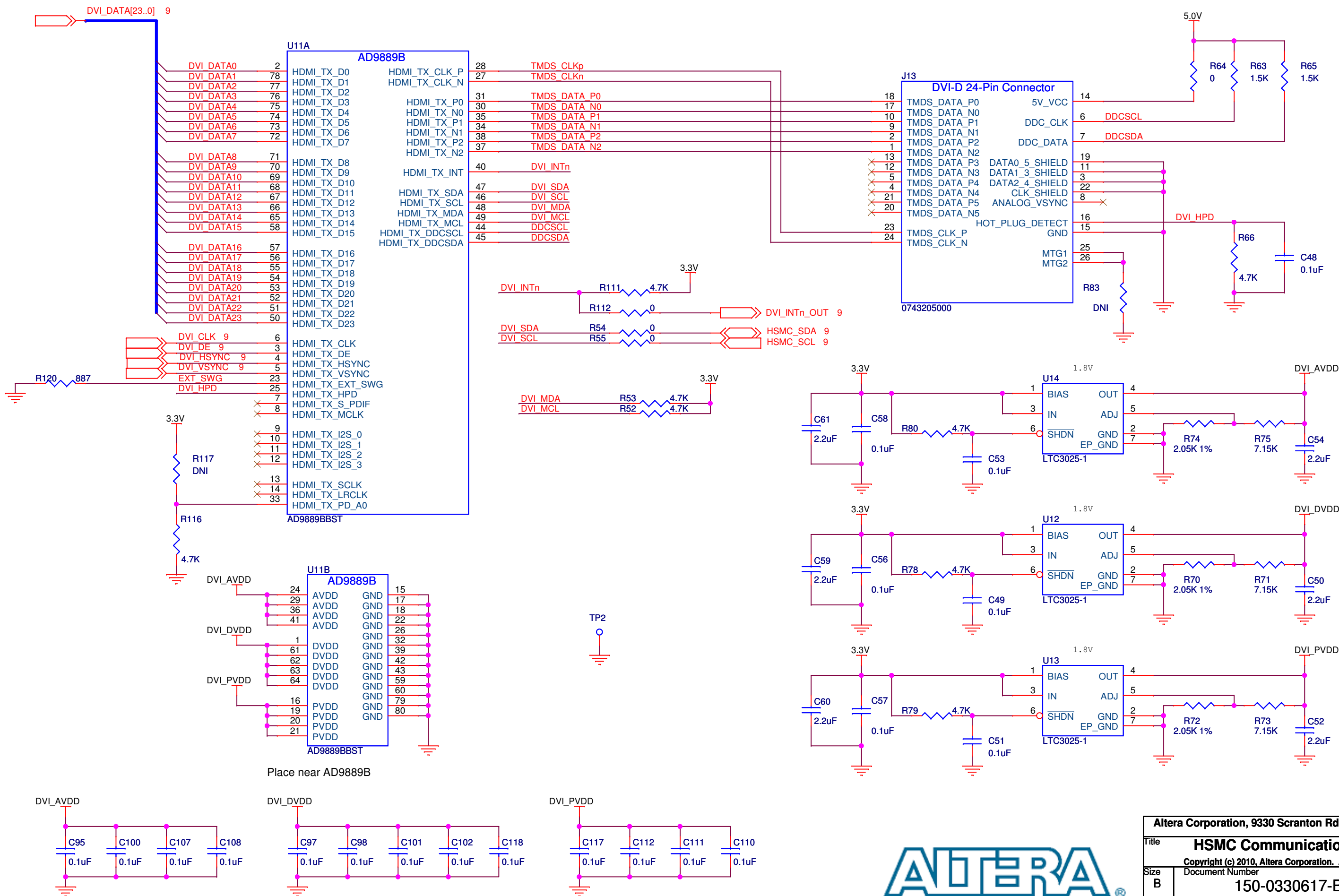
CAN

CAN INTERFACE

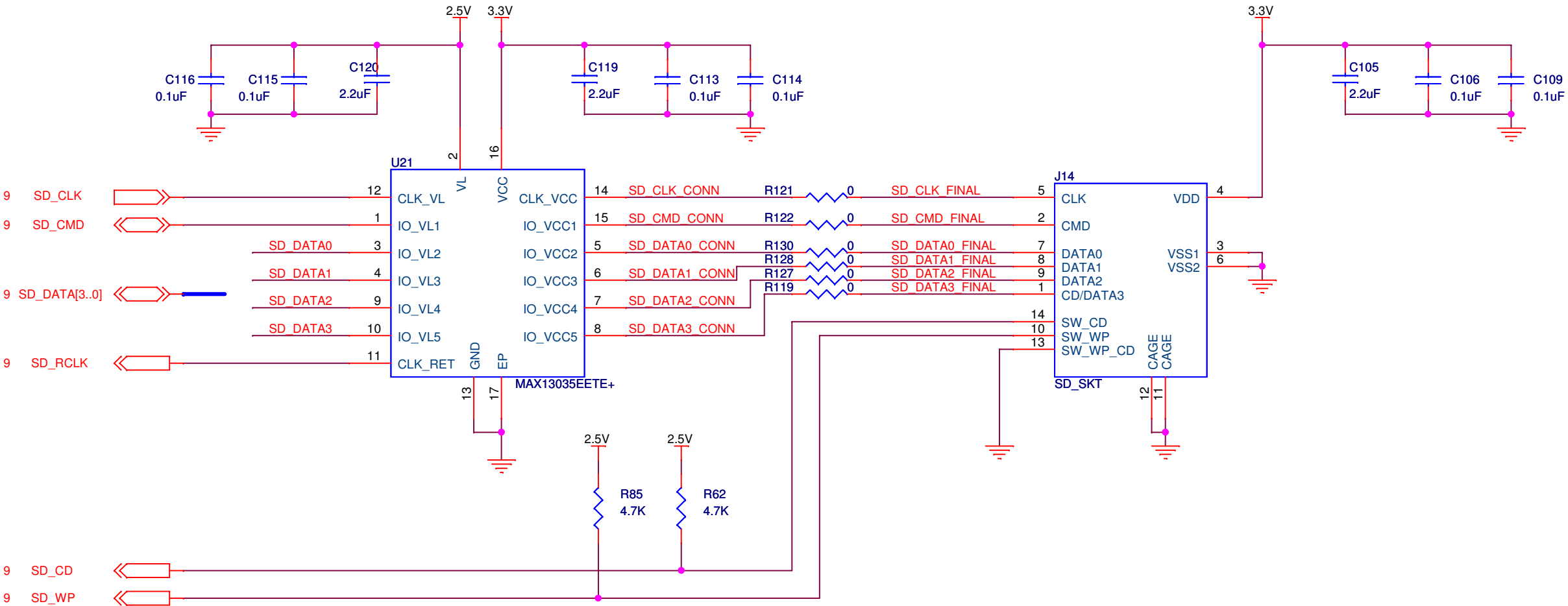


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DVI



SD/SDIO/MMC Card Reader



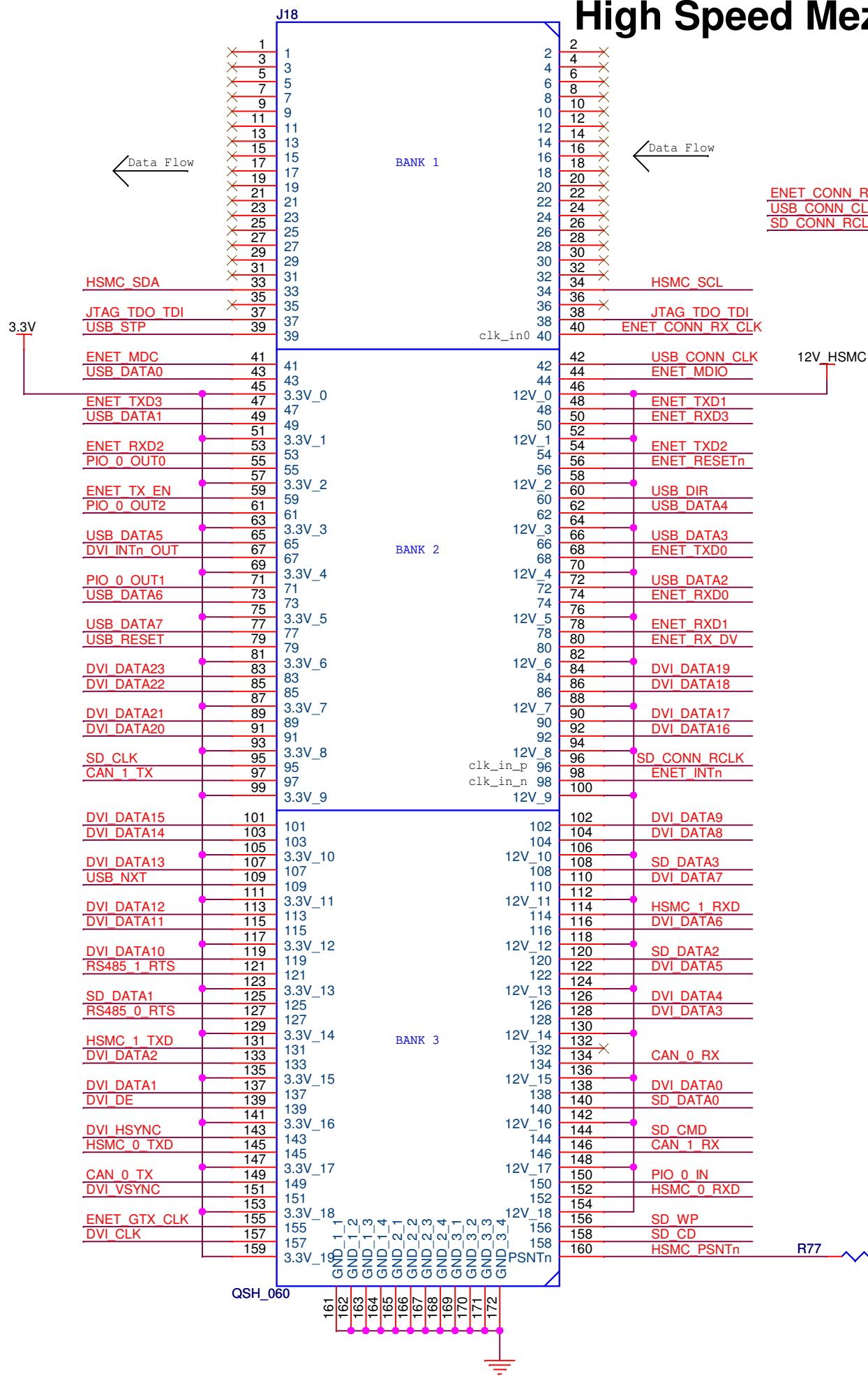
Optional Level Translator bypass.

SD_CLK	R123	DNI	SD_CLK_CONN
SD_CMD	R124	DNI	SD_CMD_CONN
SD_DATA0	R129	DNI	SD_DATA0_CONN
SD_DATA1	R126	DNI	SD_DATA1_CONN
SD_DATA2	R125	DNI	SD_DATA2_CONN
SD_DATA3	R118	DNI	SD_DATA3_CONN

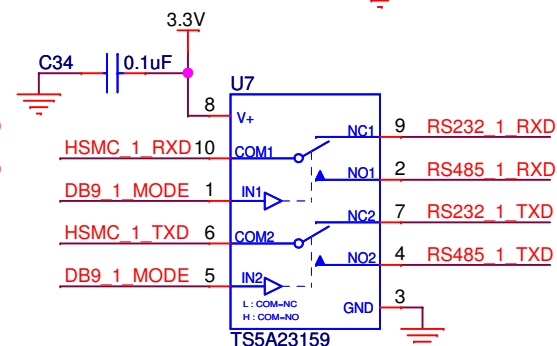
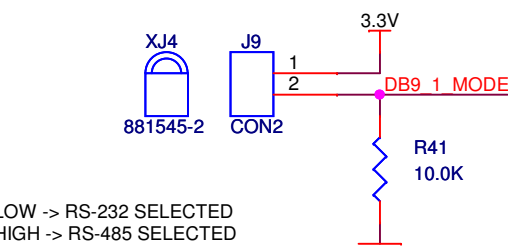
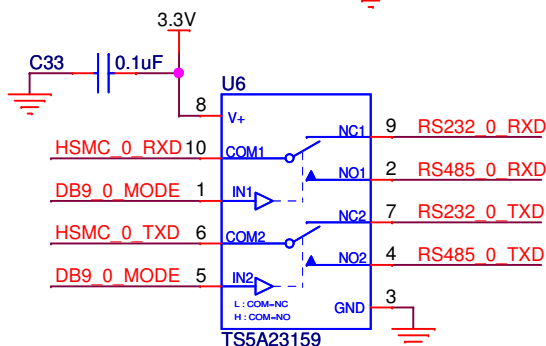
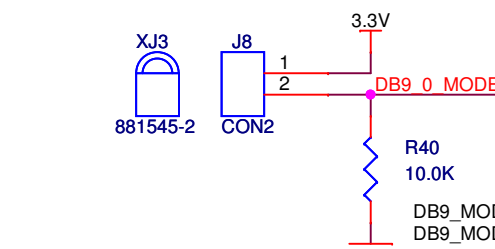
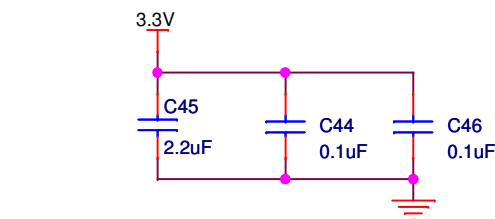


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High Speed Mezzanine Card (HSMC)

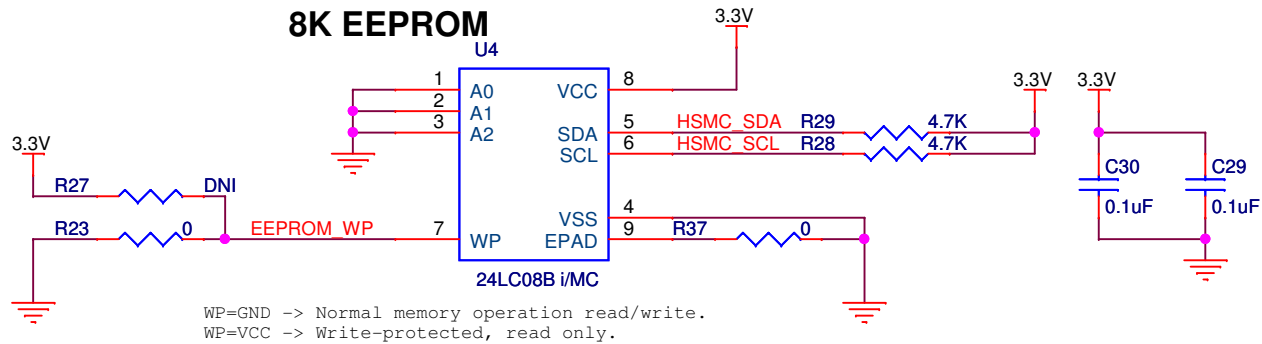


ENET_CONN_RX_CLK R86 0 ENET_RX_CLK 125MHz (max)
USB_CONN_CLK R39 0 USB_CLK 60MHz
SD_CONN_RCLK R106 0 SD_RCLK 26MHz



EEPROM is intended for use with Altera host boards to indentify the daughter card.

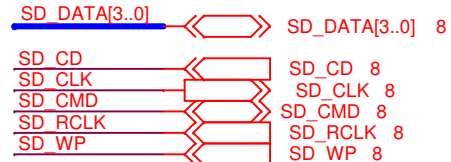
8K EEPROM



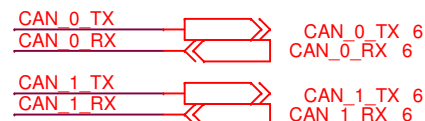
WP=GND -> Normal memory operation read/write.
WP=VCC -> Write-protected, read only.



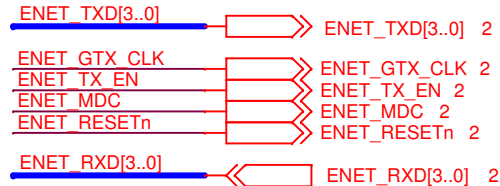
SD/SDIO/MMC INTERFACE



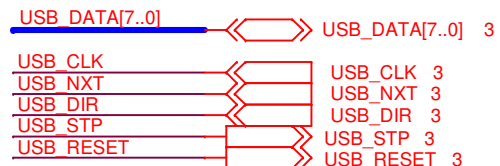
CAN INTERFACE



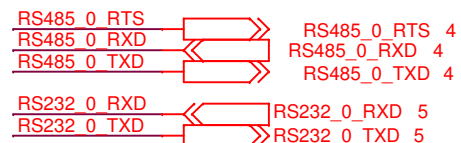
ETHERNET INTERFACE



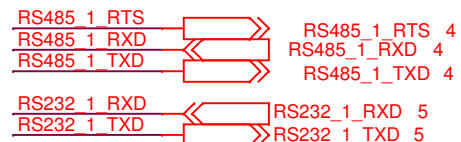
USB INTERFACE



RS-485/RS-232



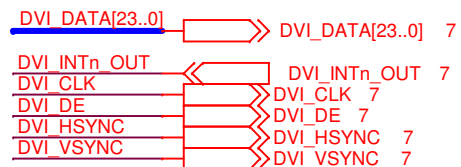
RS-485 PROFIBUS/RS-232



GPIO INTERFACE



DVI INTERFACE

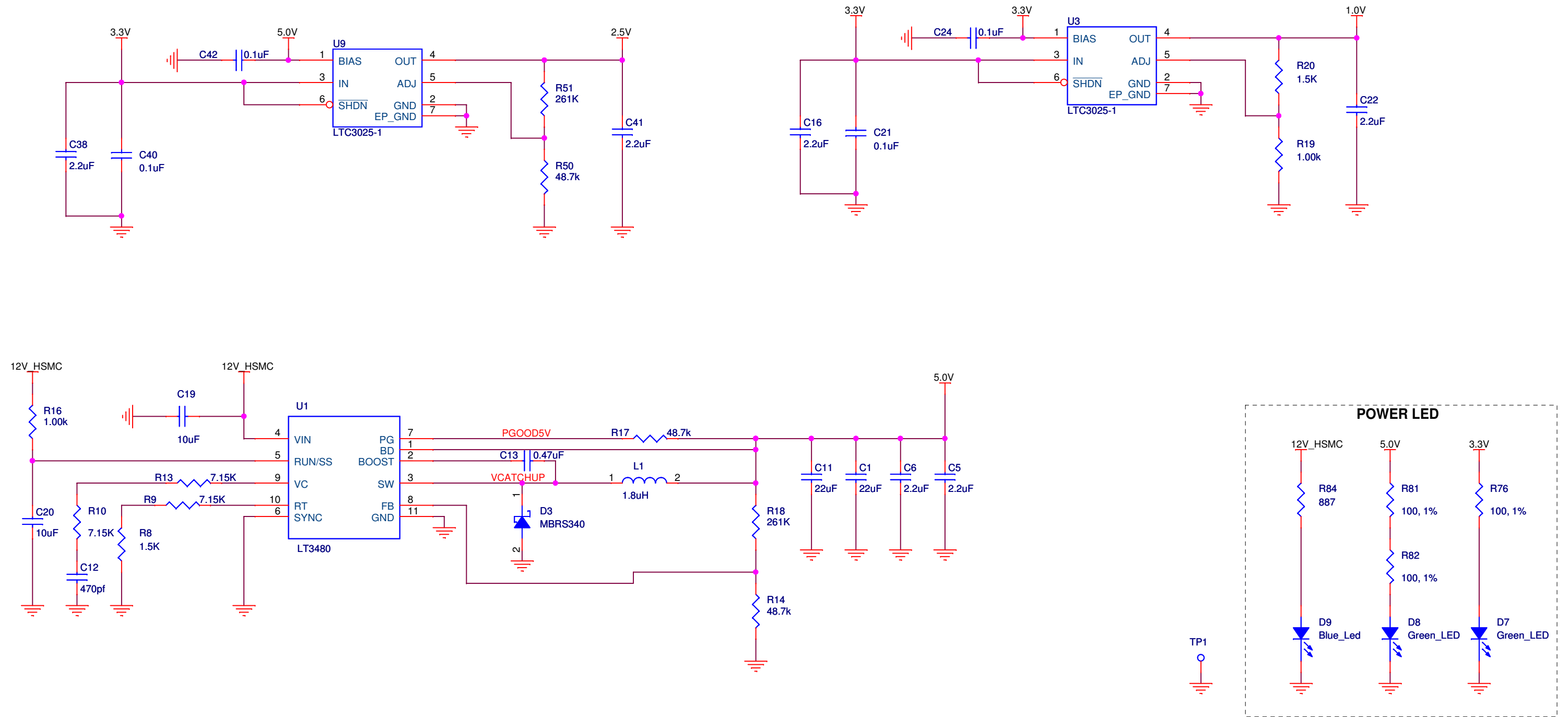


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Power 1

SCREW1	STANDOFF1
SCREW2	STANDOFF2
SCREW3	STANDOFF3
SCREW4	STANDOFF4
SCREW5	STANDOFF5



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