

Demonstration and User Manual Lookup Table

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FPGA Folder	
FPGA\DE10_Standard_ADC	Chapter 5, DE10-Standard_User_manual.pdf
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FPGA\DE10_Standard_Default	Chapter 5, DE10-Standard_User_manual.pdf
FPGA\DE10_Standard_DRAM_RTL_Test	Chapter 5, DE10-Standard_User_manual.pdf
FPGA\DE10_Standard_i2sound	Chapter 5, DE10-Standard_User_manual.pdf
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FPGA\DE10_Standard_PS2	Chapter 5, DE10-Standard_User_manual.pdf
FPGA\DE10_Standard_TV	Chapter 5, DE10-Standard_User_manual.pdf
FPGA\DE10_Standard_VIP_TV	Chapter 5, DE10-Standard_User_manual.pdf
FPGA\Factory_batch	Chapter 5, DE10-Standard_User_manual.pdf
FPGA\golden_top	Chapter 4, DE10-Standard_User_manual.pdf
FPGA\my_first_fpga	DE10-Standard_My_First_Fpga.pdf
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SoC Folder	
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SoC_Advanced\NET_Time	Chapter 6, DE10-Standard_User_manual.pdf
SoC_Advanced\openCV	DE10-Standard_OpenCV.pdf
SoC_FPGA Folder	
SoC_FPGA\ControlPanel	DE10-Standard_Control_Panel.pdf
SoC_FPGA\DE10_Standard_FB	Chapter 9, DE10-Standard_User_manual.pdf
SoC_FPGA\DE10_Standard_GHRD	Chapter 7, DE10-Standard_User_manual.pdf
SoC_FPGA\HPS_FPGA_LED	Chapter 7, DE10-Standard_User_manual.pdf