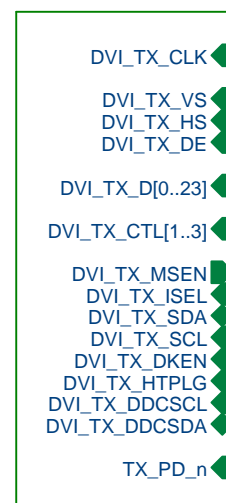


Terasic DVI TX and RX Daughter Board

SCHEMATIC	CONTENT	PAGE
01 Top	Cover Page, TOP	01 ~ 02
02 HSMC	HSMC	03 ~ 04
03 DVI Transmitter	TI TFP410	05
03 DVI Recriver	TI TFP401A	06

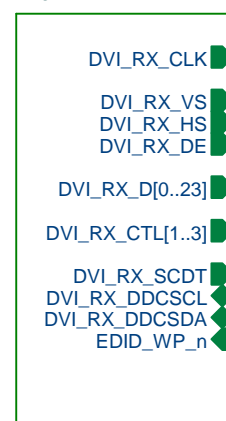
Revision History		
Dates	Version	Changes

Page 4



03 DVI Transmitter

Page 5



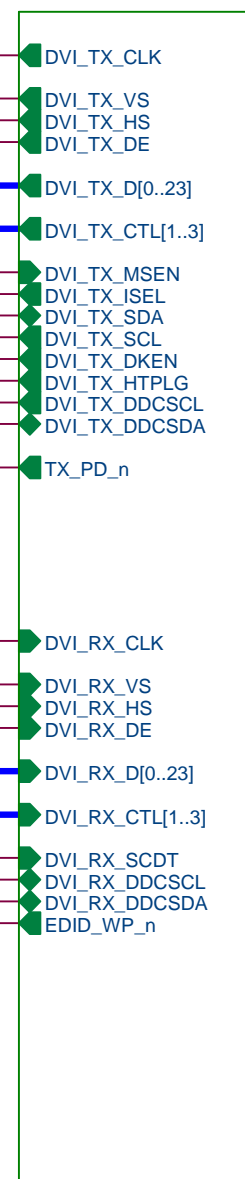
04 DVI Receiver

Page 6




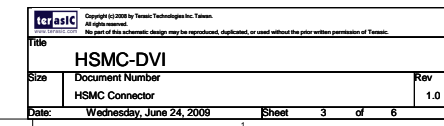
05 Power

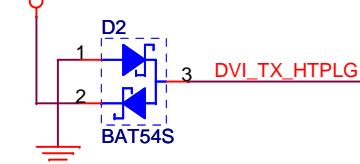
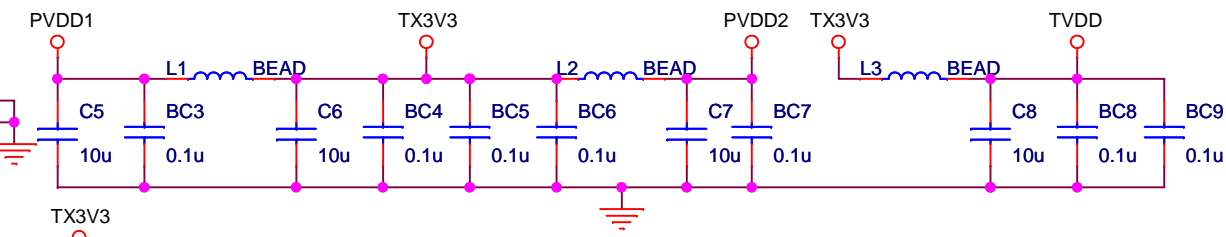
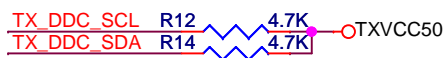
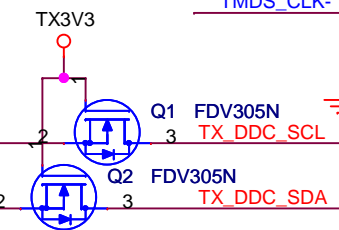
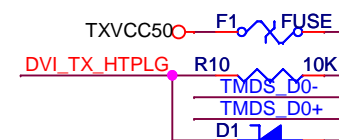
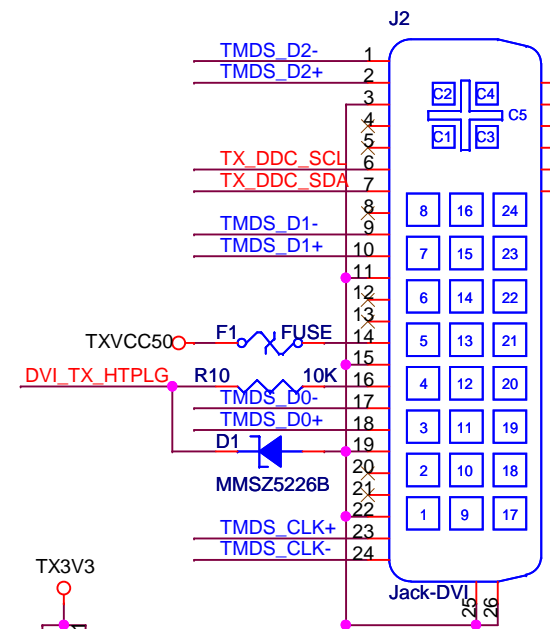
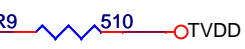
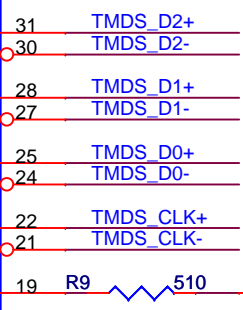
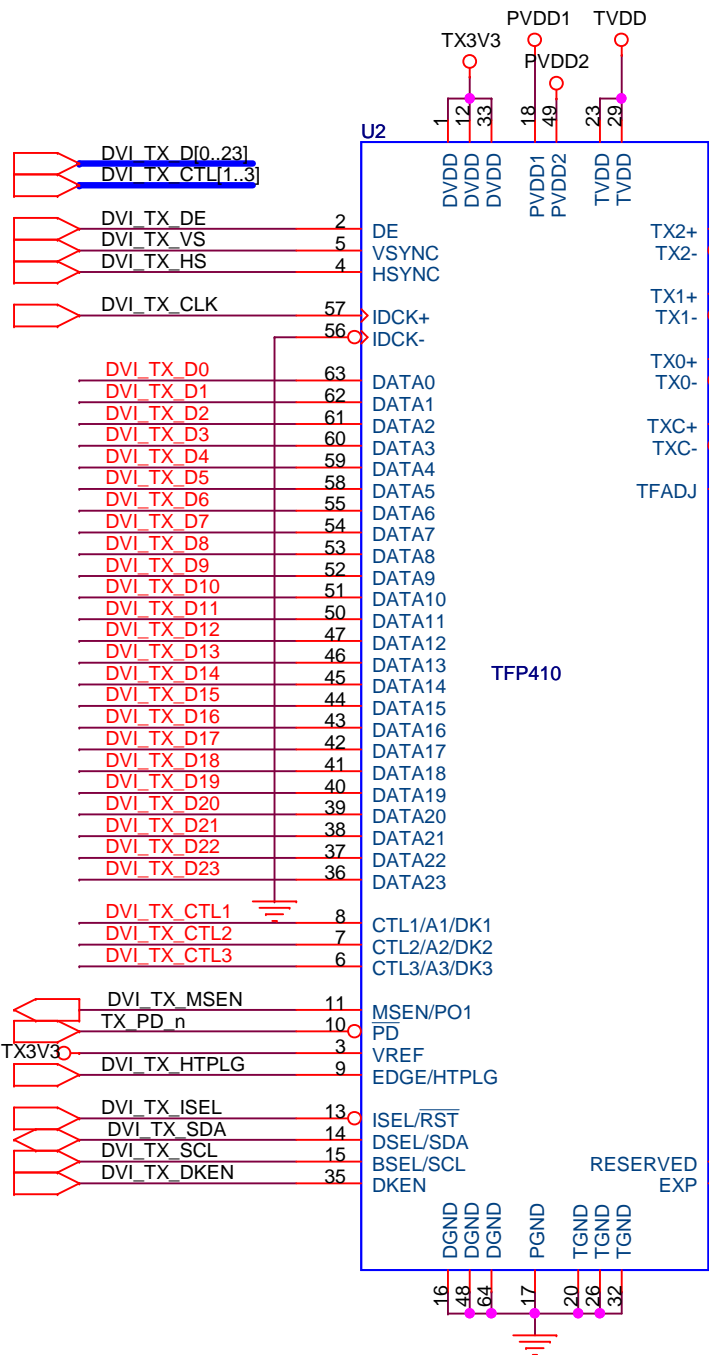
Page 3




02 HSMC Connector

 www.terasic.com		Copyright (c) 2008 by Terasic Technologies Inc. Taiwan. All rights reserved. No part of this schematic design may be reproduced, duplicated, or used without the prior written permission of Terasic.	
Title HSMC-DVI			
Size A	Document Number TOP		Rev 1.0
Date:	Wednesday, June 24, 2009	Sheet 2	of 6





PIN	NAME
1	TDM D2-
2	TDM D2+
3	Shield
4	TDM D4-
5	TDM D4+
6	DDC CLK
7	DDC DATA
8	Analog VS
9	TDM D1-
10	TDM D1+
11	Shield
12	TDM D3-
13	TDM D3+
14	Power +5V
15	Ground
16	Hot Plug Detect
17	TDM D0-
18	TDM D0+
19	Shield
20	TDM D5-
21	TDM D5+
22	Shield
23	TDM CLK+
24	TDM CLK-
C1	Analog RED
C2	Analog GREEN
C3	Analog BLUE
C4	Analog HS
C5	Analog Ground
C6	Analog Ground



Copyright (c) 2008 by Terasic Technologies Inc. Taiwan.
All rights reserved.
No part of this schematic design may be reproduced, duplicated, or used without the prior written permission of Terasic.

Title

HSMC-DVI

Size

A

Document Number

TI TFP410 (DVI TX)

Rev

1.0

Date:

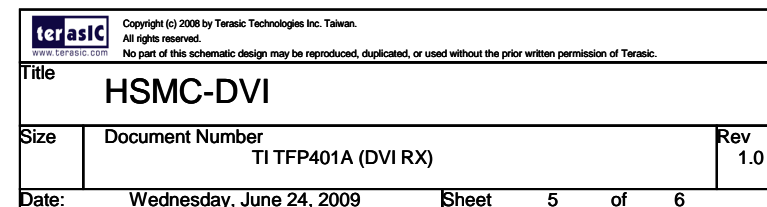
Wednesday, June 24, 2009

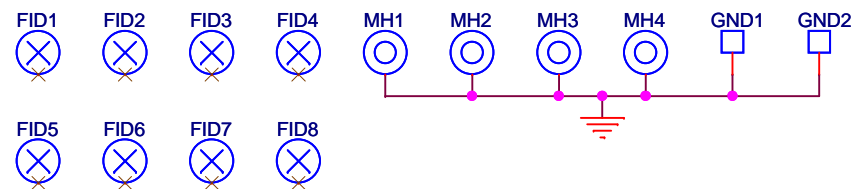
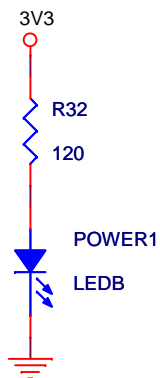
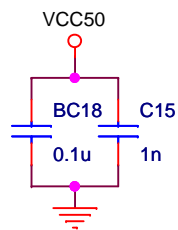
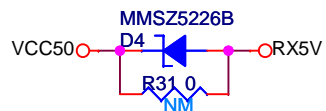
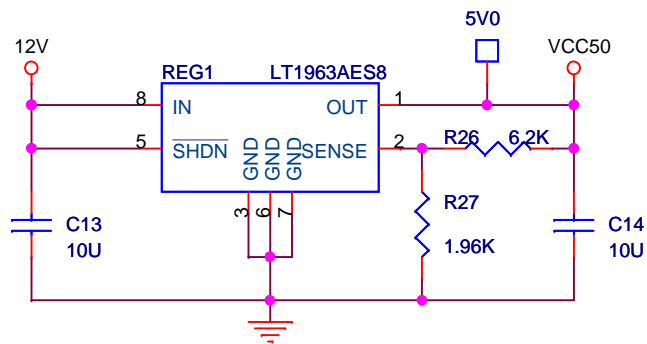
Sheet


4

of

6





 Copyright (c) 2008 by Terasic Technologies Inc. Taiwan. All rights reserved. No part of this schematic design may be reproduced, duplicated, or used without the prior written permission of Terasic.		
Title		
HSMC-DVI		
Size	Document Number	Rev
A	5V & 3.3V	1.0
Date:	Wednesday, June 24, 2009	Sheet 6 of 6