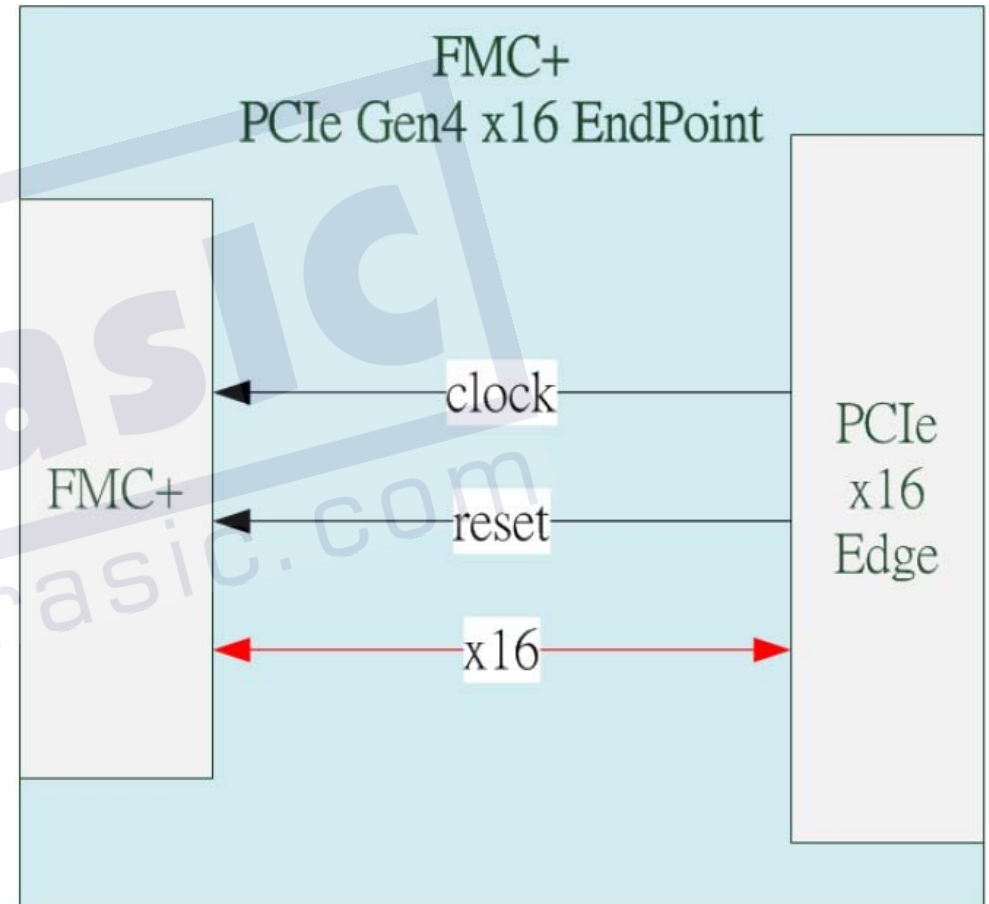


### ***FMC + to PCIe x 16 Edge, Endpoint***


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1	Cover Page
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3	FMC+ Page.1
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5	PCIe x 16, Endpoint
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### ***Block Diagram***

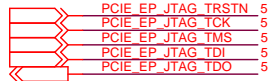
[illegible]

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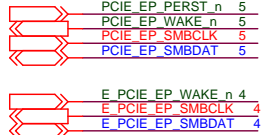


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Title					
FMC+ to PCIe x 16 Edge, Endpoint					
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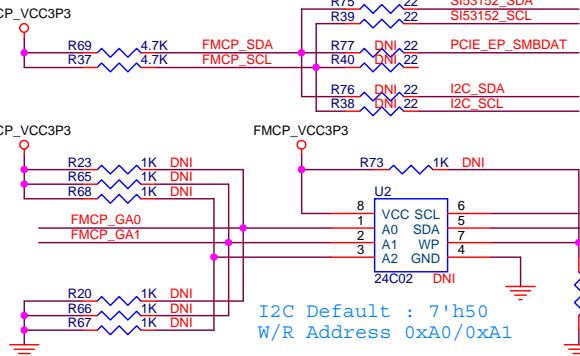
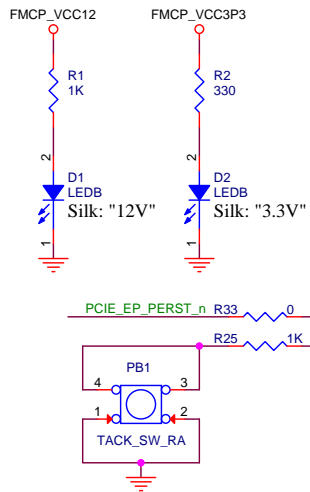
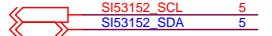
## PCIe JTAG



## PCIe Control signal



## SI53152 I2C Interface

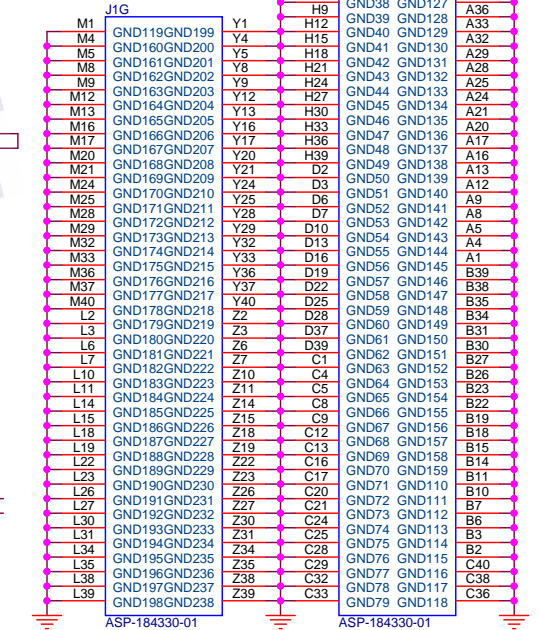
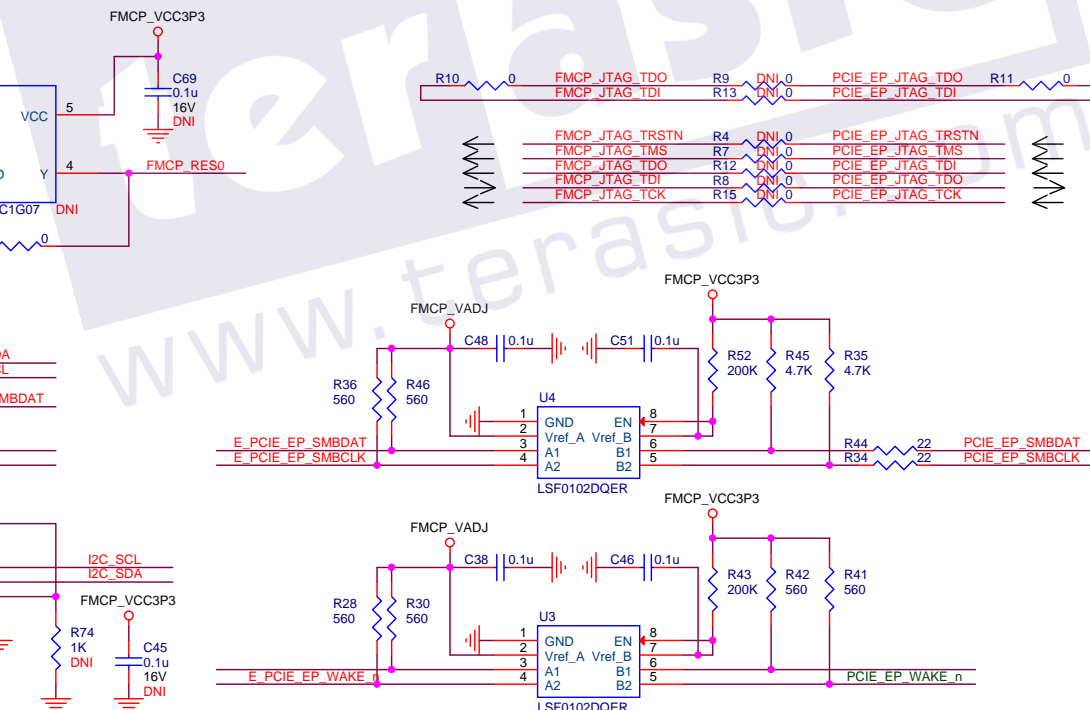
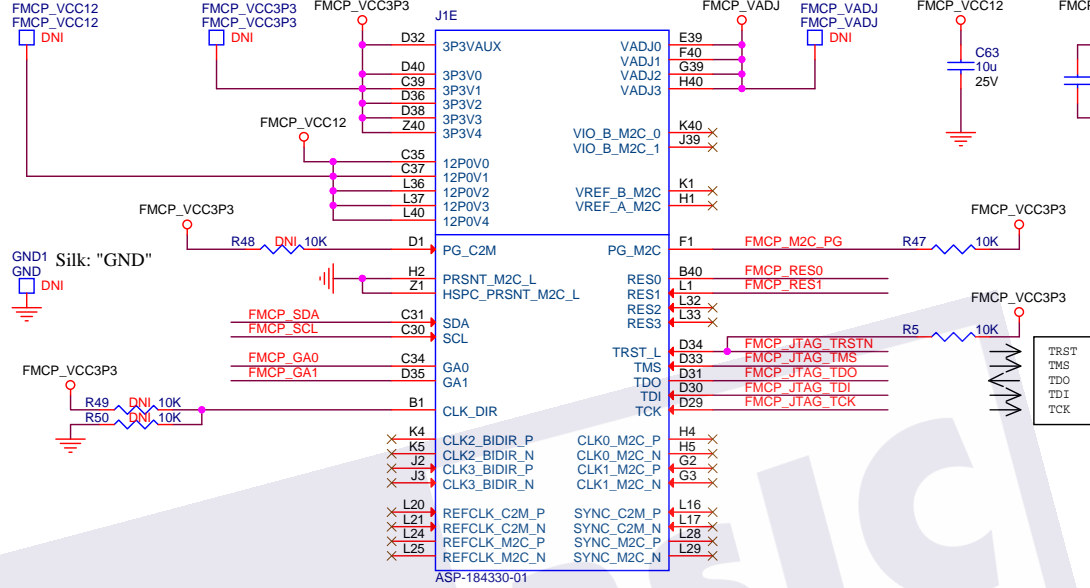


I2C Default : 7'h50  
W/R Address 0xA0/0xA1

Silk: "FMCP\_VCC12"

Silk: "FMCP\_VCC3P3"

Silk: "FMCP\_VADJ"



# PCIe Transceiver

PCIE EP TX p[15..0] 5  
PCIE EP TX n[15..0] 5  
PCIE EP RX p[15..0] 5  
PCIE EP RX n[15..0] 5

PCIE EP REFCLK p[1..0] 5  
PCIE EP REFCLK n[1..0] 5

PCIE EP RSV1 5  
PCIE EP RSV3 5

E PCIE EP SMBCLK 3  
E PCIE EP SMBDAT 3

E PCIE EP WAKE n 3

PCIE CLKREQ\_n  
PCIE PWRBRKN

E PCIE EP SMBCLK  
E PCIE EP SMBDAT  
E PCIE EP WAKE\_n

K25	HB_P0_CC	HB_P11	J30
K26	HB_N0_CC	HB_N11	J31
J24	HB_P1	HB_P12	F31
J25	HB_P1	HB_P12	F32
F22	HB_N1	HB_N12	E30
F23	HB_P2	HB_P13	E31
E21	HB_N2	HB_N13	K34
E22	HB_P3	HB_P14	K35
	HB_N3	HB_N14	
F25	HB_P4	HB_P15	J33
F26	HB_N4	HB_N15	J34
E24	HB_P5	HB_P16	F34
E25	HB_N5	HB_N16	F35
K28	HB_P6_CC	HB_P17_CC	K37
K29	HB_N6_CC	HB_N17_CC	K38
J27	HB_P7	HB_P18	J36
J28	HB_N7	HB_N18	J37
F28	HB_P8	HB_P19	E33
F29	HB_N8	HB_N19	E34
E27	HB_P9	HB_P20	F37
E28	HB_N9	HB_N20	F38
K31	HB_P10	HB_P21	E36
K32	HB_N10	HB_N21	E37

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G6	LA_P0_CC	LA_P16	G18
G7	LA_N0_CC	LA_N16	G19
D8	LA_P1_CC	LA_P17_CC	D20
D9	LA_N1_CC	LA_N17_CC	D21
H7	LA_P2	LA_P18_CC	C22
H8	LA_N2	LA_N18_CC	C23
G9	LA_P3	LA_P19	H22
G10	LA_N3	LA_N19	H23
H10	LA_P4	LA_P20	G21
H11	LA_N4	LA_N20	G22
D11	LA_P5	LA_P21	H25
D12	LA_N5	LA_N21	H26
C10	LA_P6	LA_P22	G24
C11	LA_N6	LA_N22	G25
H13	LA_P7	LA_P23	D23
H14	LA_N7	LA_N23	D24
G12	LA_P8	LA_P24	H28
G13	LA_N8	LA_N24	H29
D14	LA_P9	LA_P25	G27
D15	LA_N9	LA_N25	G28
C14	LA_P10	LA_P26	D26
C15	LA_N10	LA_N26	D27
H16	LA_P11	LA_P27	C26
H17	LA_N11	LA_N27	C27
G15	LA_P12	LA_P28	H31
G16	LA_N12	LA_N28	H32
D17	LA_P13	LA_P29	G30
D18	LA_N13	LA_N29	G31
C18	LA_P14	LA_P30	H34
C19	LA_N14	LA_N30	H35
H19	LA_P15	LA_P31	G33
H20	LA_N15	LA_N31	G34
		LA_P32	H37
		LA_N32	H38
		LA_P33	G36
		LA_N33	G37

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F4	HA_P0_CC	HA_P12	F13
F5	HA_N0_CC	HA_N12	F14
E2	HA_P1_CC	HA_P13	E12
E3	HA_N1_CC	HA_N13	E13
K7	HA_P2	HA_P14	J15
K8	HA_N2	HA_N14	J16
J6	HA_P3	HA_P15	F16
J7	HA_N3	HA_N15	F17
F7	HA_P4	HA_P16	E15
F8	HA_N4	HA_N16	E16
E6	HA_P5	HA_P17_CC	K16
E7	HA_N5	HA_N17_CC	K17
K10	HA_P6	HA_P18	J18
K11	HA_N6	HA_N18	J19
J9	HA_P7	HA_P19	F19
J10	HA_N7	HA_N19	F20
F11	HA_P8	HA_P20	E18
F11	HA_N8	HA_N20	E19
E9	HA_P9	HA_P21	K19
E10	HA_N9	HA_N21	K20
K13	HA_P10	HA_P22	J21
K14	HA_N10	HA_N22	J22
J12	HA_P11	HA_P23	K22
J13	HA_N11	HA_N23	K23

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PCIE EP TX p0	C2	DP0_C2M_P	DP0_M2C_P	C6	PCIE EP RX p0
PCIE EP TX n0	C3	DP0_C2M_N	DP0_M2C_N	C7	PCIE EP RX n0
PCIE EP TX p1	A22	DP1_C2M_P	DP1_M2C_P	A2	PCIE EP RX p1
PCIE EP TX n1	A23	DP1_C2M_N	DP1_M2C_N	A3	PCIE EP RX n1
PCIE EP TX p2	A26	DP2_C2M_P	DP2_M2C_P	A6	PCIE EP RX p2
PCIE EP TX n2	A27	DP2_C2M_N	DP2_M2C_N	A7	PCIE EP RX n2
PCIE EP TX p3	A30	DP3_C2M_P	DP3_M2C_P	A10	PCIE EP RX p3
PCIE EP TX n3	A31	DP3_C2M_N	DP3_M2C_N	A11	PCIE EP RX n3
PCIE EP TX p4	A34	DP4_C2M_P	DP4_M2C_P	A14	PCIE EP RX p4
PCIE EP TX n4	A35	DP4_C2M_N	DP4_M2C_N	A15	PCIE EP RX n4
PCIE EP TX p5	A38	DP5_C2M_P	DP5_M2C_P	A18	PCIE EP RX p5
PCIE EP TX n5	A39	DP5_C2M_N	DP5_M2C_N	A19	PCIE EP RX n5
PCIE EP TX p6	B36	DP6_C2M_P	DP6_M2C_P	B16	PCIE EP RX p6
PCIE EP TX n6	B37	DP6_C2M_N	DP6_M2C_N	B17	PCIE EP RX n6
PCIE EP TX p7	B32	DP7_C2M_P	DP7_M2C_P	B12	PCIE EP RX p7
PCIE EP TX n7	B33	DP7_C2M_N	DP7_M2C_N	B13	PCIE EP RX n7
PCIE EP TX p8	B28	DP8_C2M_P	DP8_M2C_P	B8	PCIE EP RX p8
PCIE EP TX n8	B29	DP8_C2M_N	DP8_M2C_N	B9	PCIE EP RX n8
PCIE EP TX p9	B24	DP9_C2M_P	DP9_M2C_P	B4	PCIE EP RX p9
PCIE EP TX n9	B25	DP9_C2M_N	DP9_M2C_N	B5	PCIE EP RX n9
PCIE EP TX p10	Z24	DP10_C2M_P	DP10_M2C_P	Y10	PCIE EP RX p10
PCIE EP TX n10	Z25	DP10_C2M_N	DP10_M2C_N	Y11	PCIE EP RX n10
PCIE EP TX p11	Y26	DP11_C2M_P	DP11_M2C_P	Z12	PCIE EP RX p11
PCIE EP TX n11	Y27	DP11_C2M_N	DP11_M2C_N	Z13	PCIE EP RX n11
PCIE EP TX p12	Z28	DP12_C2M_P	DP12_M2C_P	Y14	PCIE EP RX p12
PCIE EP TX n12	Z29	DP12_C2M_N	DP12_M2C_N	Y15	PCIE EP RX n12
PCIE EP TX p13	Y30	DP13_C2M_P	DP13_M2C_P	Z16	PCIE EP RX p13
PCIE EP TX n13	Y31	DP13_C2M_N	DP13_M2C_N	Z17	PCIE EP RX n13
PCIE EP TX p14	M18	DP14_C2M_P	DP14_M2C_P	Y18	PCIE EP RX p14
PCIE EP TX n14	M19	DP14_C2M_N	DP14_M2C_N	Y19	PCIE EP RX n14
PCIE EP TX p15	M22	DP15_C2M_P	DP15_M2C_P	Y22	PCIE EP RX p15
PCIE EP TX n15	M23	DP15_C2M_N	DP15_M2C_N	Y23	PCIE EP RX n15
	M26	DP16_C2M_P	DP16_M2C_P	Z32	
	M27	DP16_C2M_N	DP16_M2C_N	Z33	
	M30	DP17_C2M_P	DP17_M2C_P	Y34	
	M31	DP17_C2M_N	DP17_M2C_N	Y35	
	M34	DP18_C2M_P	DP18_M2C_P	Z36	
	M35	DP18_C2M_N	DP18_M2C_N	Z37	
	M38	DP19_C2M_P	DP19_M2C_P	Y38	
	M39	DP19_C2M_N	DP19_M2C_N	Y39	
	Z8	DP20_C2M_P	DP20_M2C_P	M14	
	Z9	DP20_C2M_N	DP20_M2C_N	M15	
	Y6	DP21_C2M_P	DP21_M2C_P	M10	
	Y7	DP21_C2M_N	DP21_M2C_N	M11	
	Z4	DP22_C2M_P	DP22_M2C_P	M6	
	Z5	DP22_C2M_N	DP22_M2C_N	M7	
	Y2	DP23_C2M_P	DP23_M2C_P	M2	
	Y3	DP23_C2M_N	DP23_M2C_N	M3	
	L8	GBTCLK3_M2C_P	GBTCLK0_M2C_P	D4	PCIE EP REFCLK p0
	L9	GBTCLK3_M2C_N	GBTCLK0_M2C_N	D5	PCIE EP REFCLK n0
	L4	GBTCLK4_M2C_P	GBTCLK1_M2C_P	B20	PCIE EP REFCLK p1
	L5	GBTCLK4_M2C_N	GBTCLK1_M2C_N	B21	PCIE EP REFCLK n1
	Z20	GBTCLK5_M2C_P	GBTCLK2_M2C_P	L12	
	Z21	GBTCLK5_M2C_N	GBTCLK2_M2C_N	L13	

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