


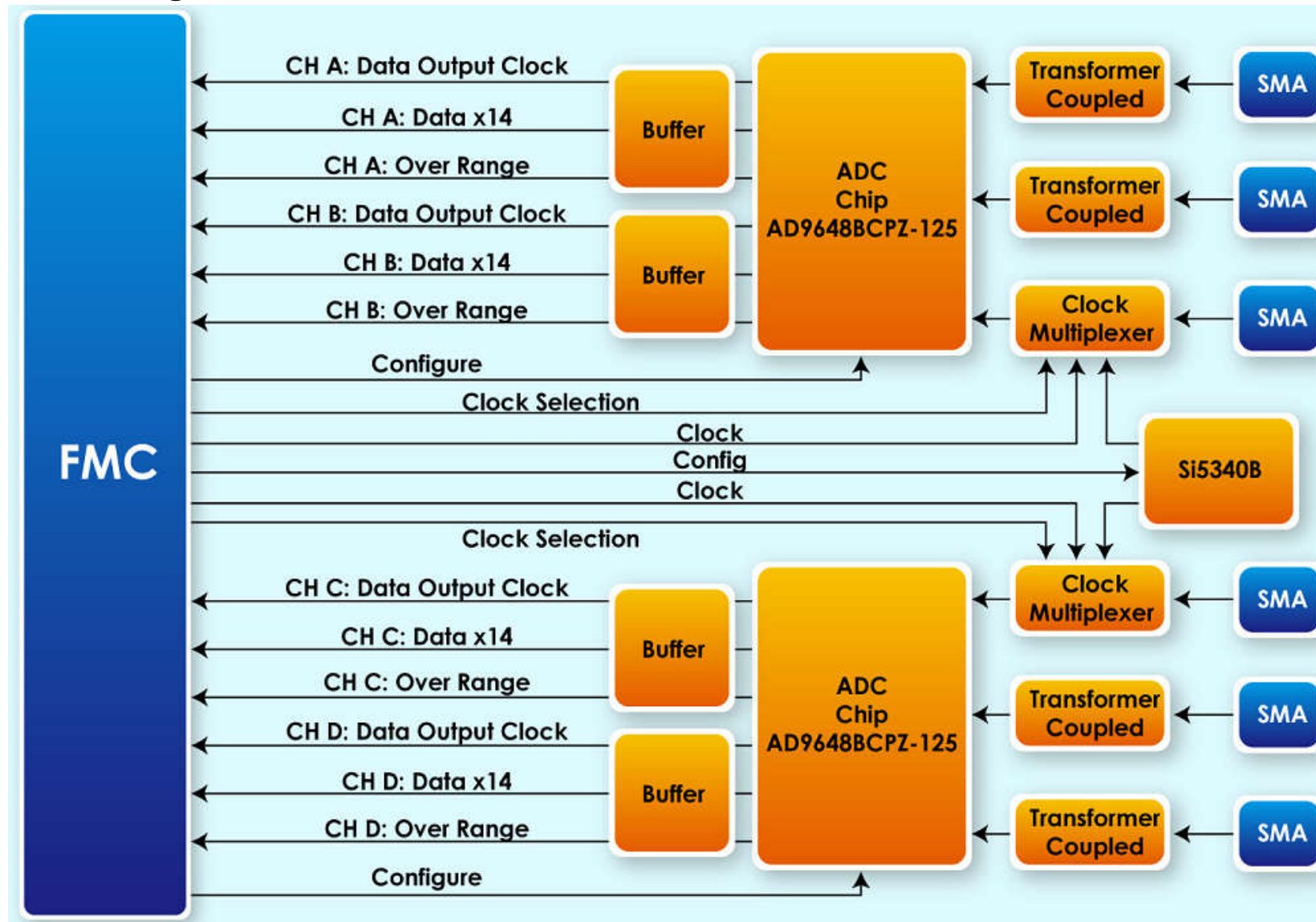
ADC-FMC

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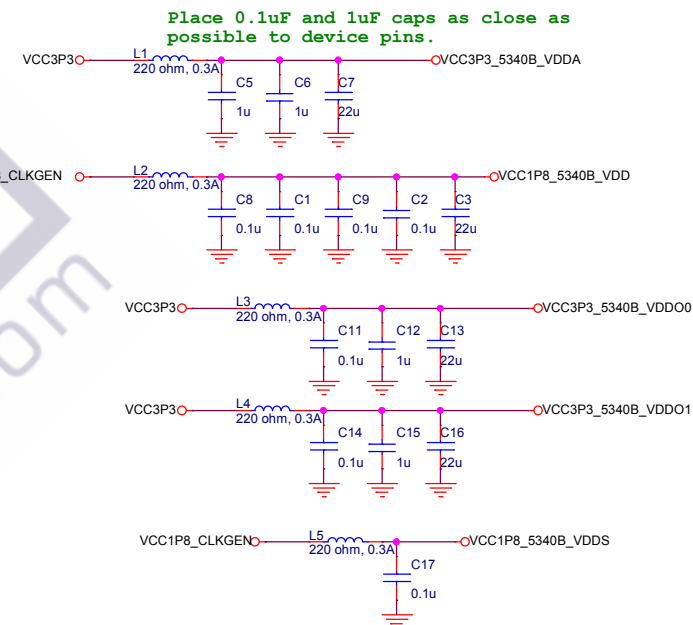
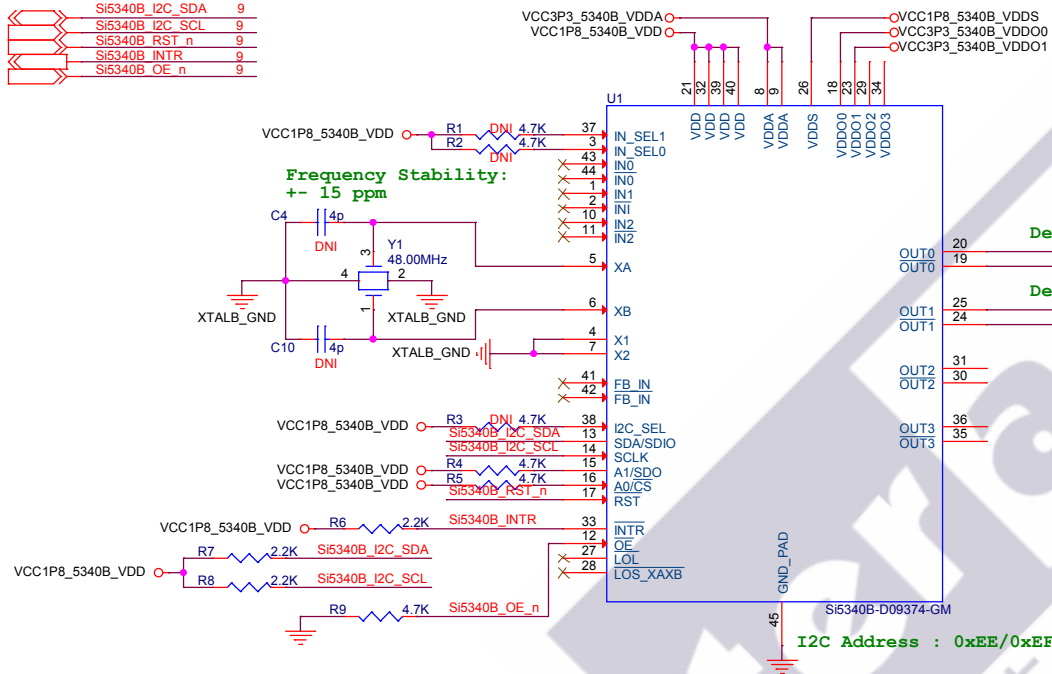
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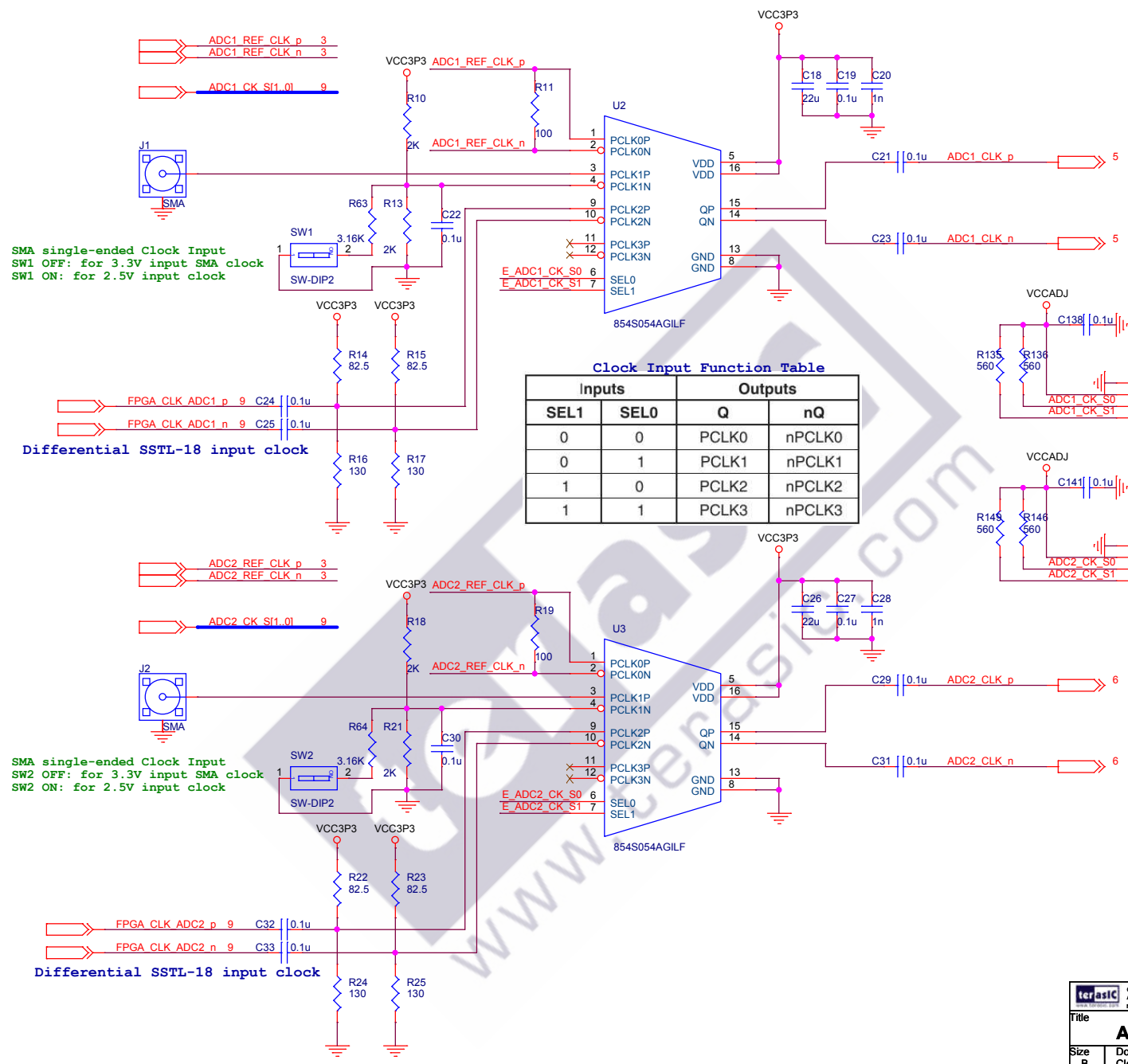
Block Diagram

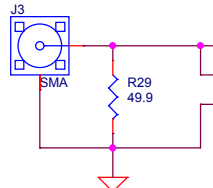


Si5340B_I2C_SDA 9
Si5340B_I2C_SCL 9
Si5340B_RST_n 9
Si5340B_INTR 9
Si5340B_OE_n 9

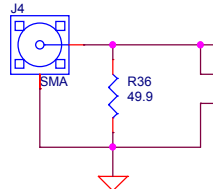
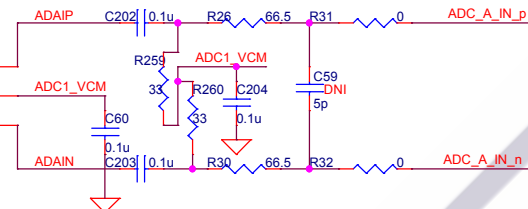


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Title			
ADC-FMC Board			
Size	Document Number		Rev
B	Clock Generator		B
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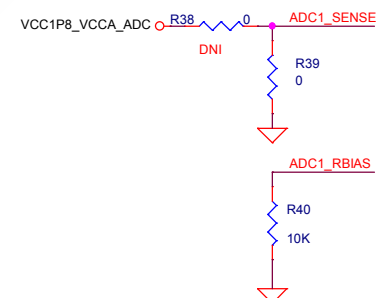
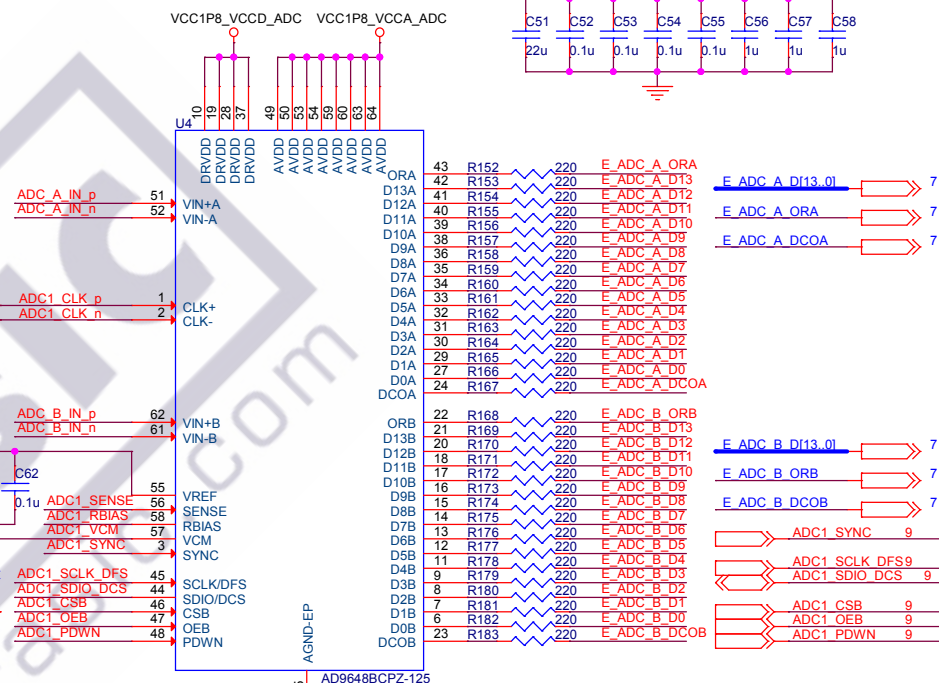
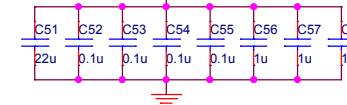
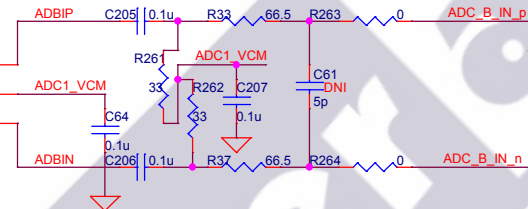


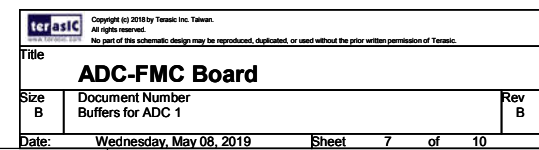
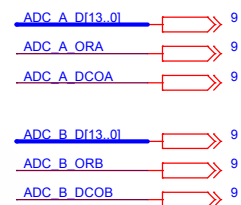


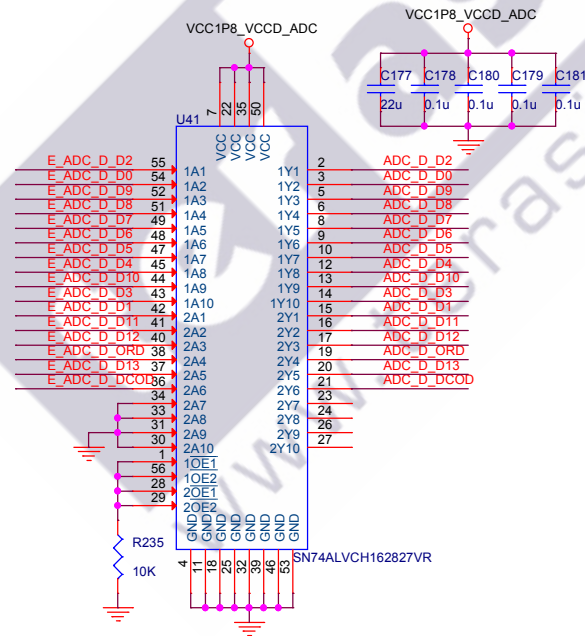
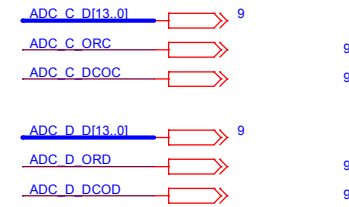
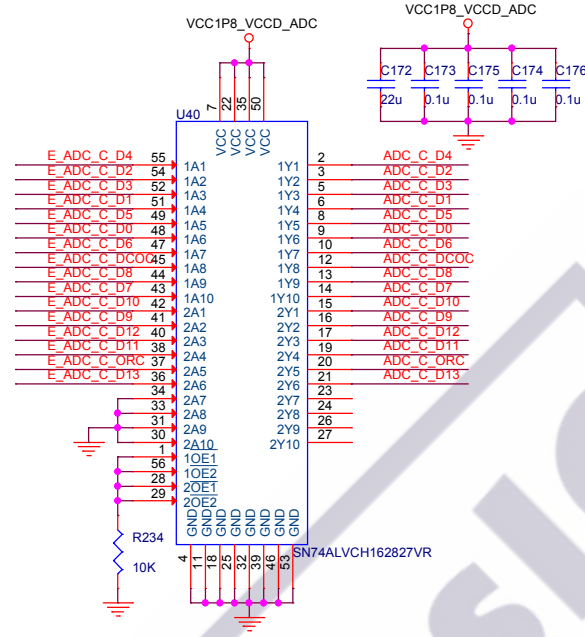
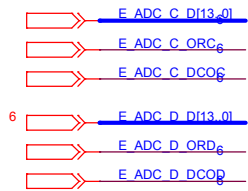
Install R27 or R28 only
to experiment for the
output swing and
characteristics




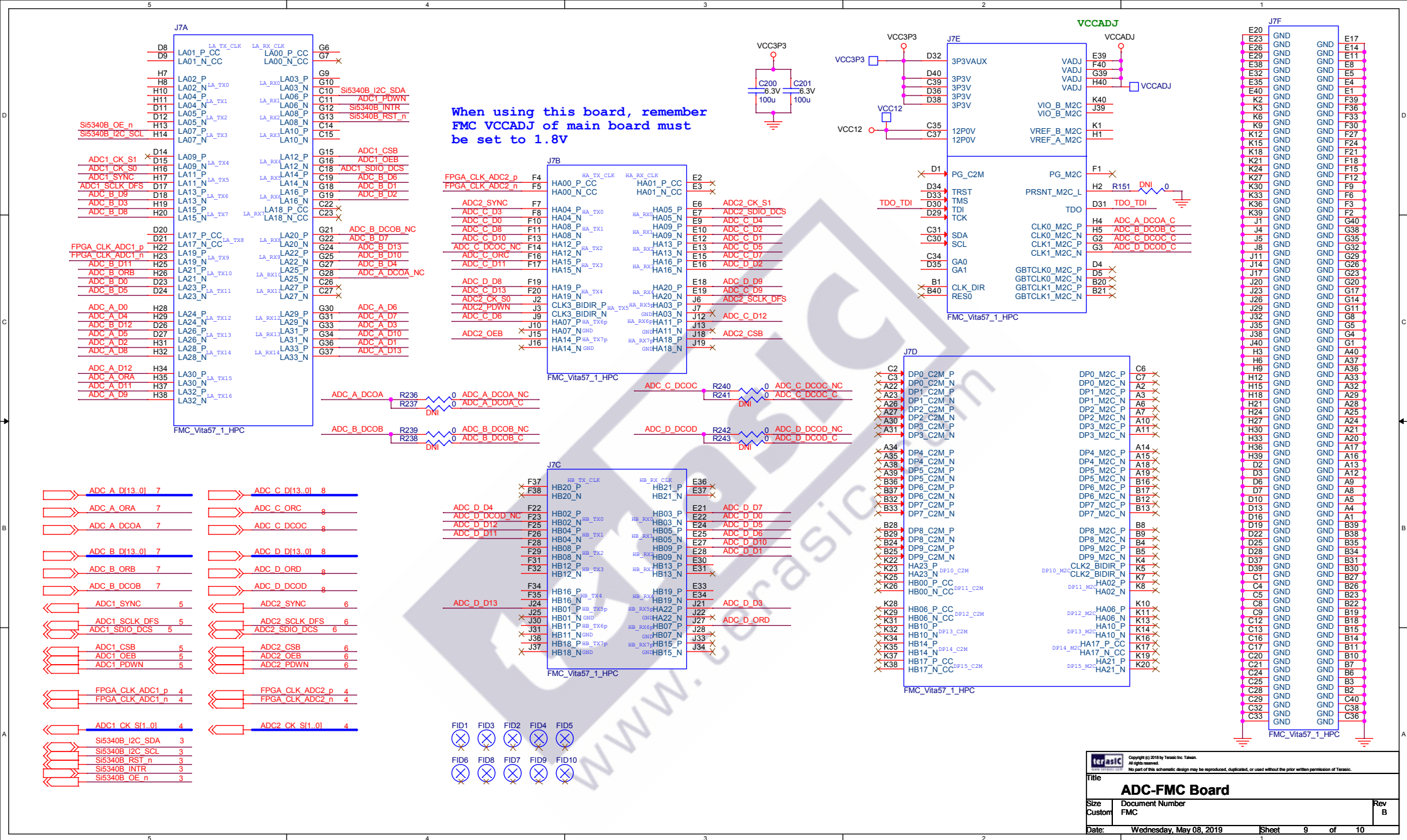
Install R34 or R35 only
to experiment for the
output swing and
characteristics

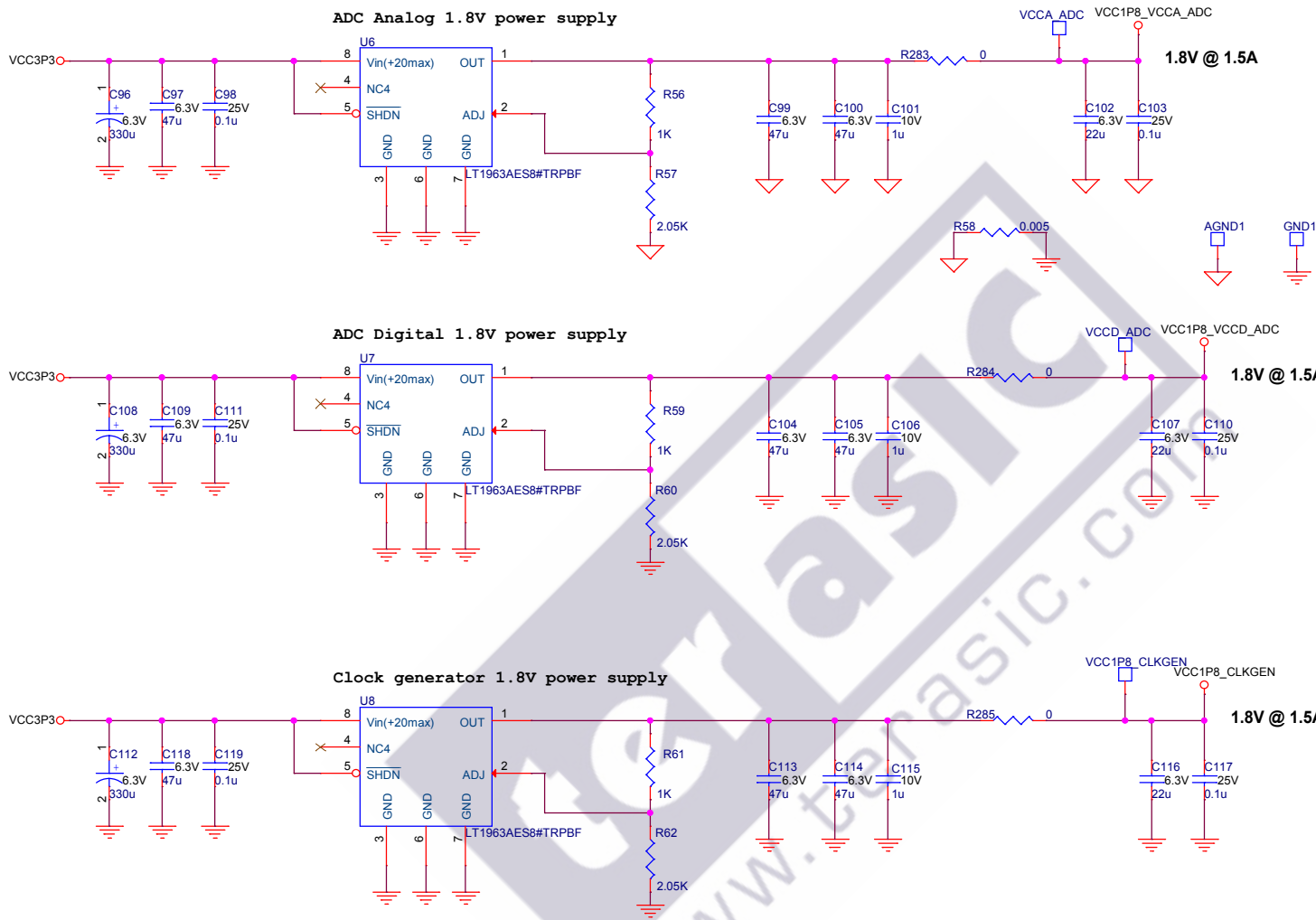






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Title				
ADC-FMC Board				
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B	Buffers for ADC 2			
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PCB1
10-21806050-B0

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ADC-FMC Board	
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