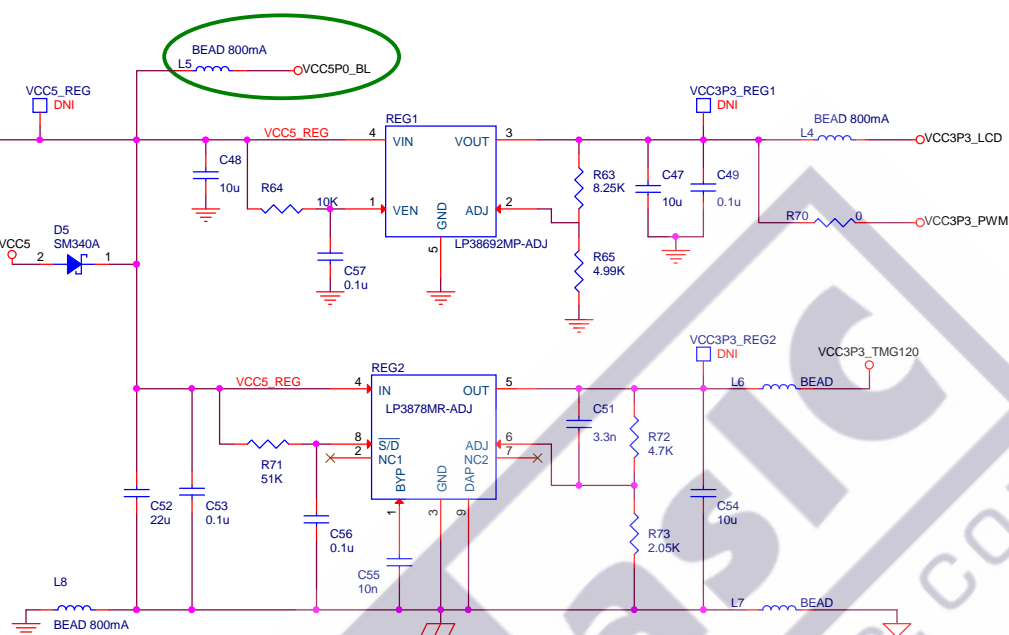
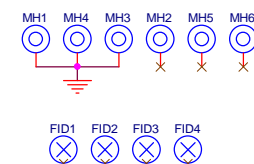
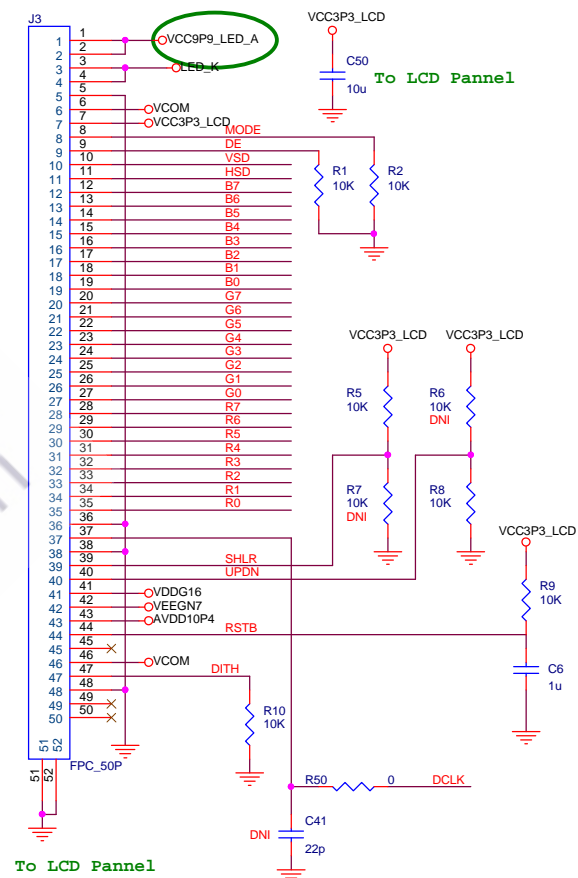
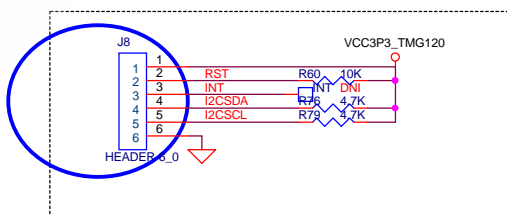
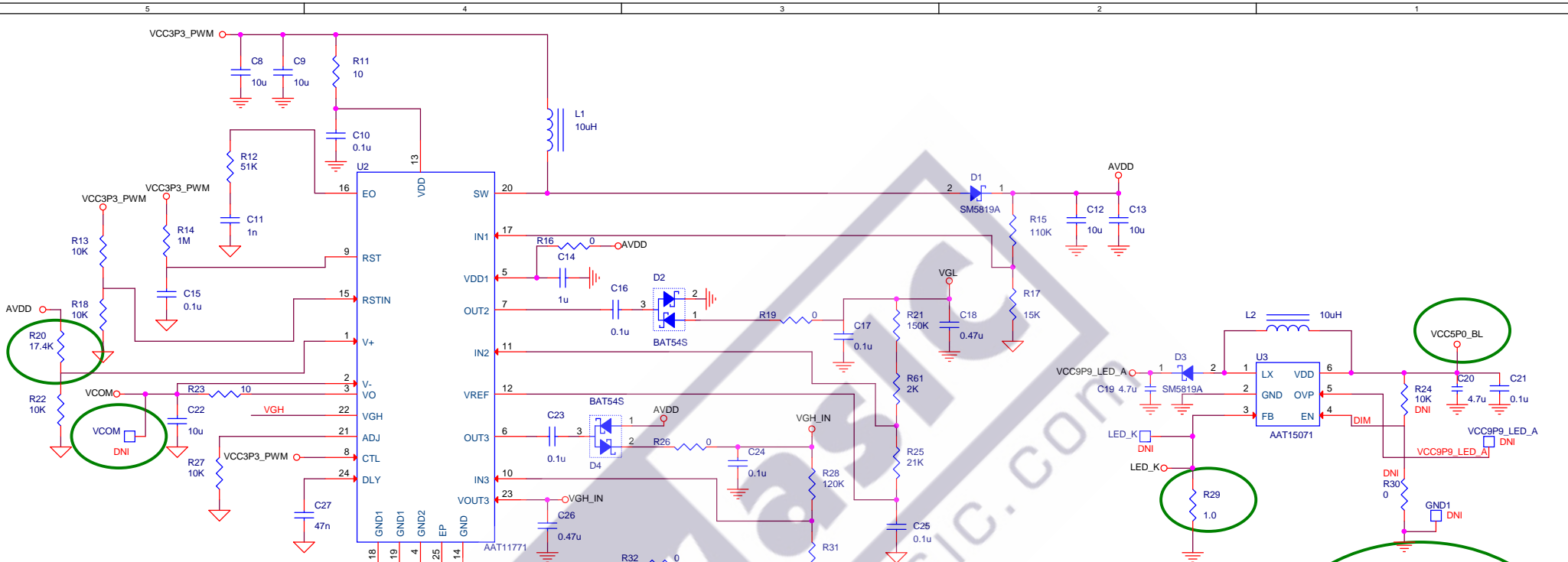


To High Speed IDE Cable

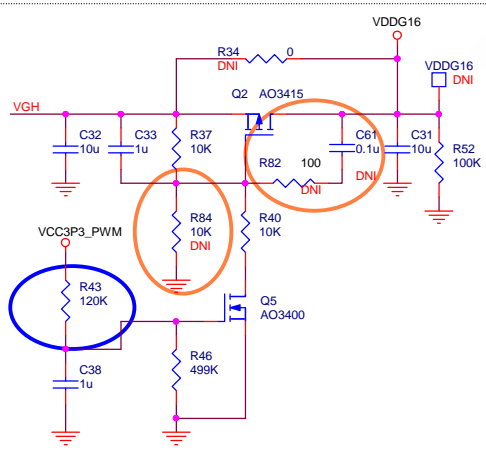


### Connection with Touch Calbe

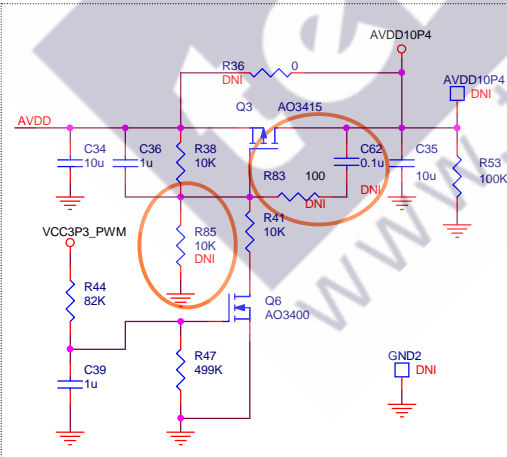




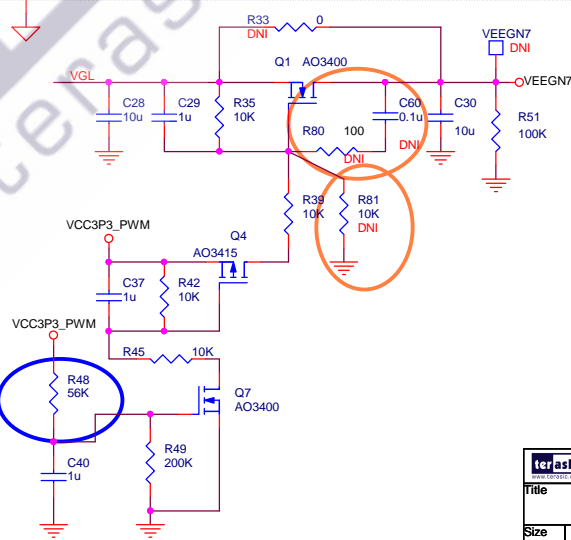
Power on sequence:  
DVDD->VGL->AVDD->VGH->BL



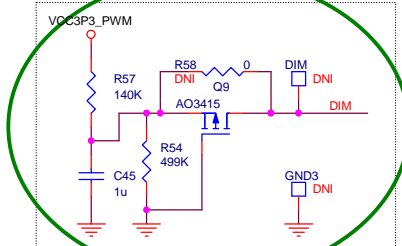
For Power on Sequence




For Power on Sequence



For Power on Sequence



For Power on Sequence

		Copyright (c) 2011 by Terasic Technologies Inc., Taiwan. All rights reserved. No part of this schematic design may be reproduced, duplicated, or used without the prior written permission of Terasic.	
Title		MTL2	
Size	Document Number	Rev	
B	LCD & Backlight Power	E	
Date:	Thursday, July 24, 2014	Sheet	2 of 2