

NOTES:

1. Project Drawing Numbers:
- Raw PCB

Gerber Files

PCB Design Files

Assembly Drawing

Fab Drawing

Schematic Drawing

PCB Film

Bill of Materials

Schematic Design Files

Functional Specification

PCB Layout Guidelines

Assembly Rework
- 100-0320610-B1

110-0320610-B1

120-0320610-B1

130-0320610-B1

140-0320610-B1

150-0320610-B1

160-0320610-B1

170-0320610-B1

180-0320610-B1

210-0320610-B1

220-0320610-B1

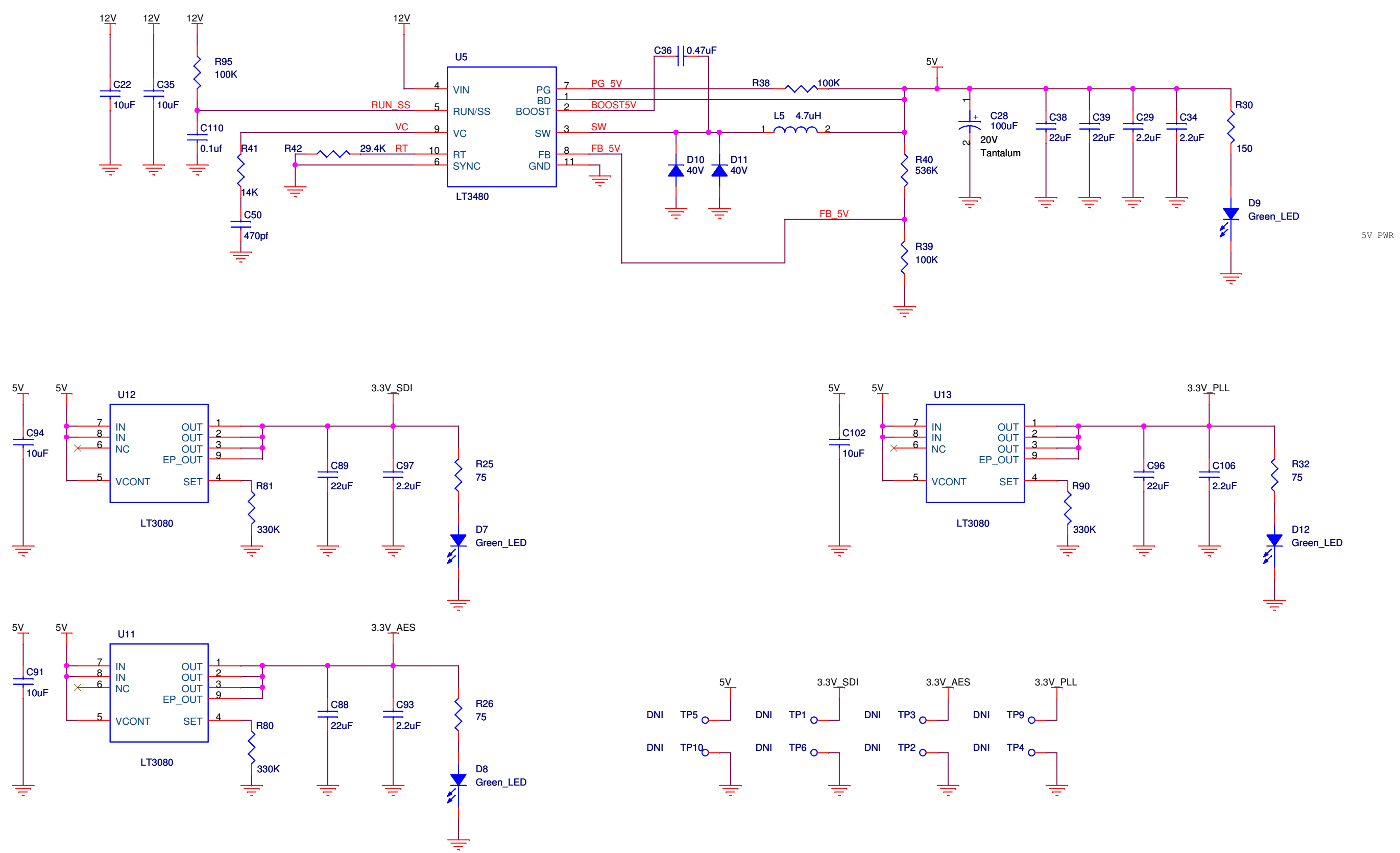
320-0320610-B1

PAGE	DESCRIPTION
1	Title, Notes, Block Diagram, Revision History
2	Power
3	RX 1 Equalizer
4	RX 2 Equalizer
5	TX 1/2 Cable Driver
6	AES3 Transceiver/Clock
7	AES3 I/O
8	Video Sync Separator
9	SDI CLOCK Generation
10	HSMC Connector
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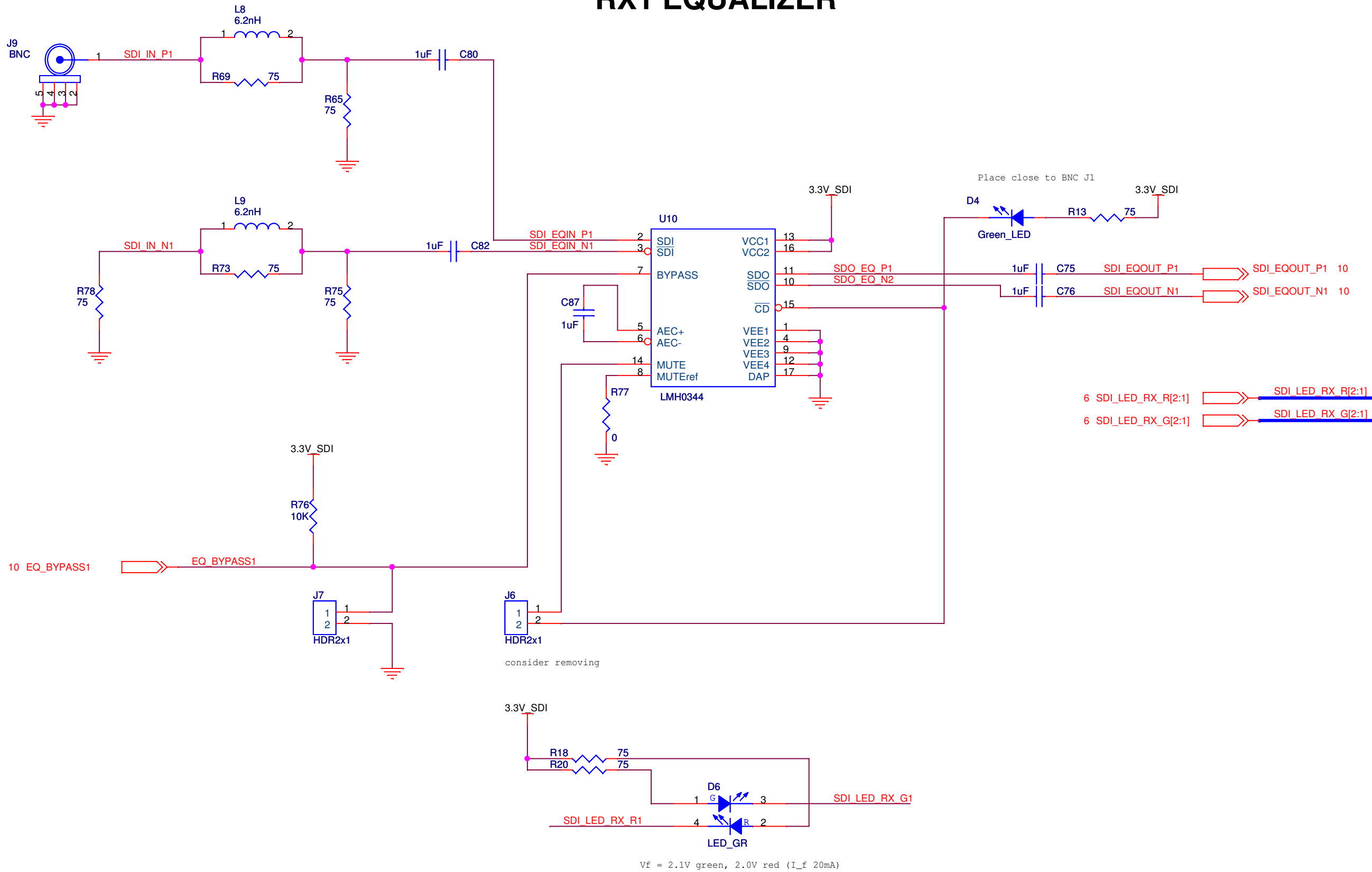
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TitleXCVR AES3/SDI HSMC		
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# POWER

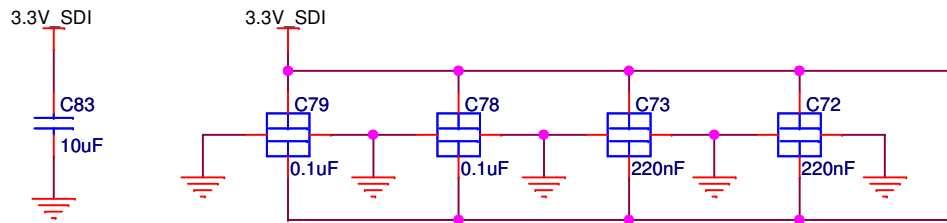


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# RX1 EQUALIZER

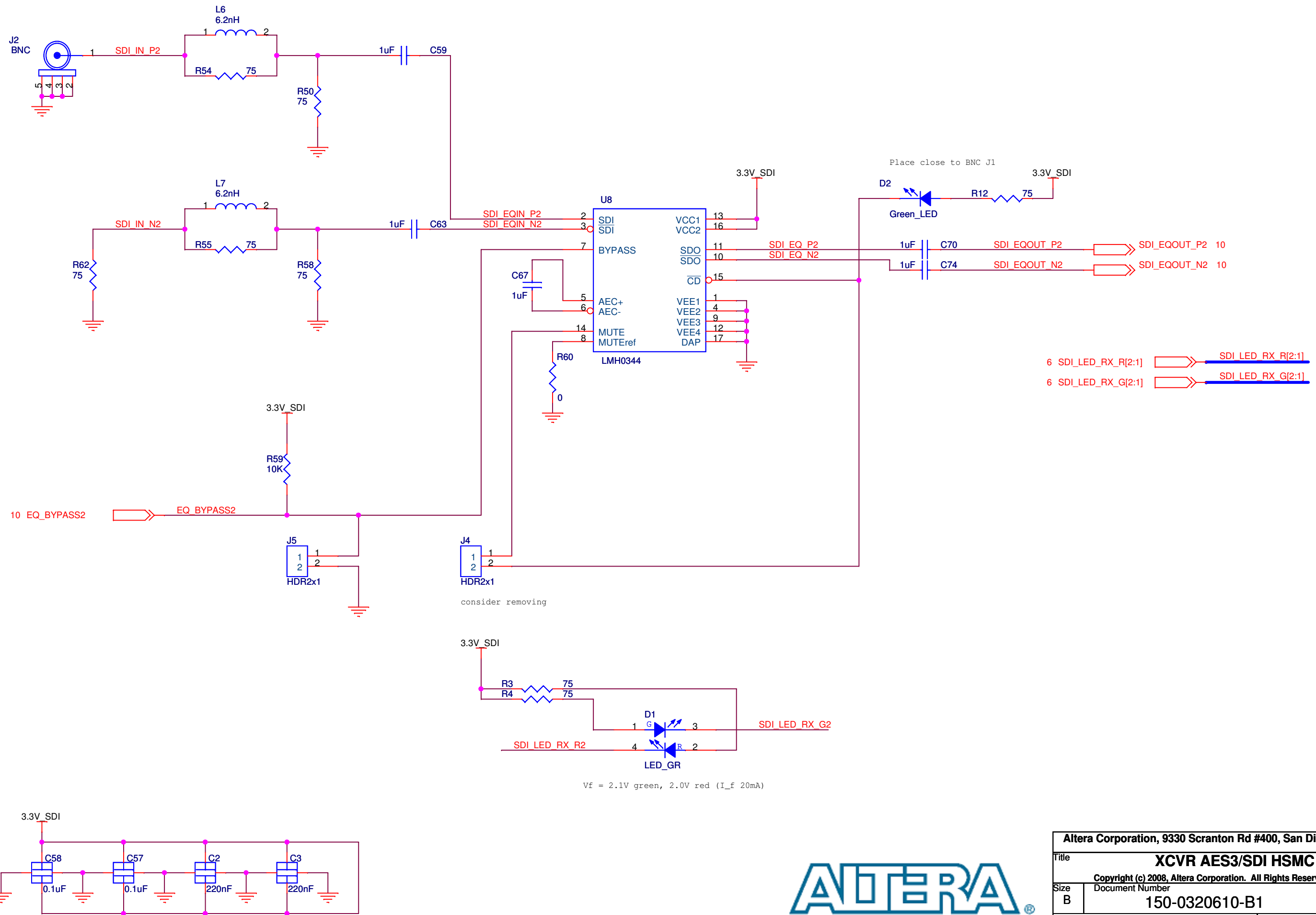


Vf = 2.1V green, 2.0V red (I\_f 20mA)

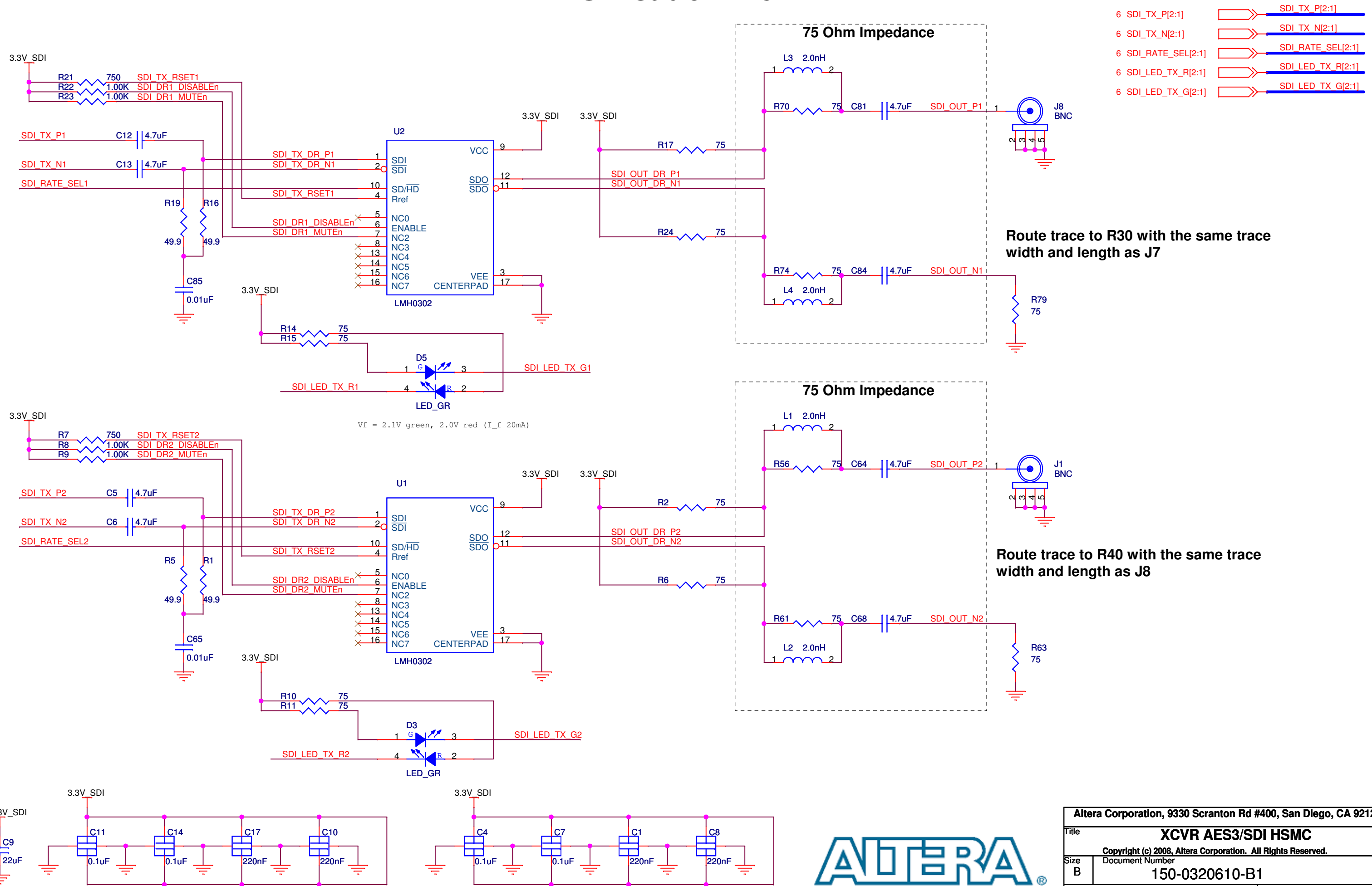


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# RX2 EQUALIZER



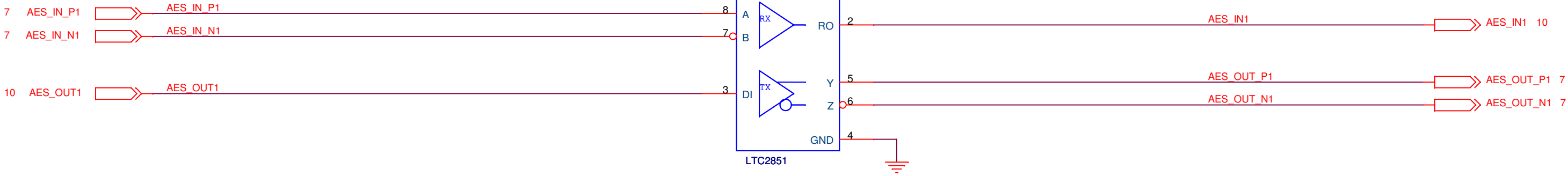
# SDI Cable Driver



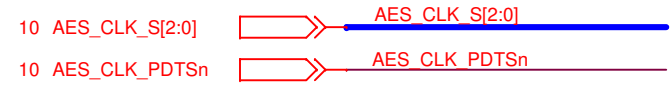
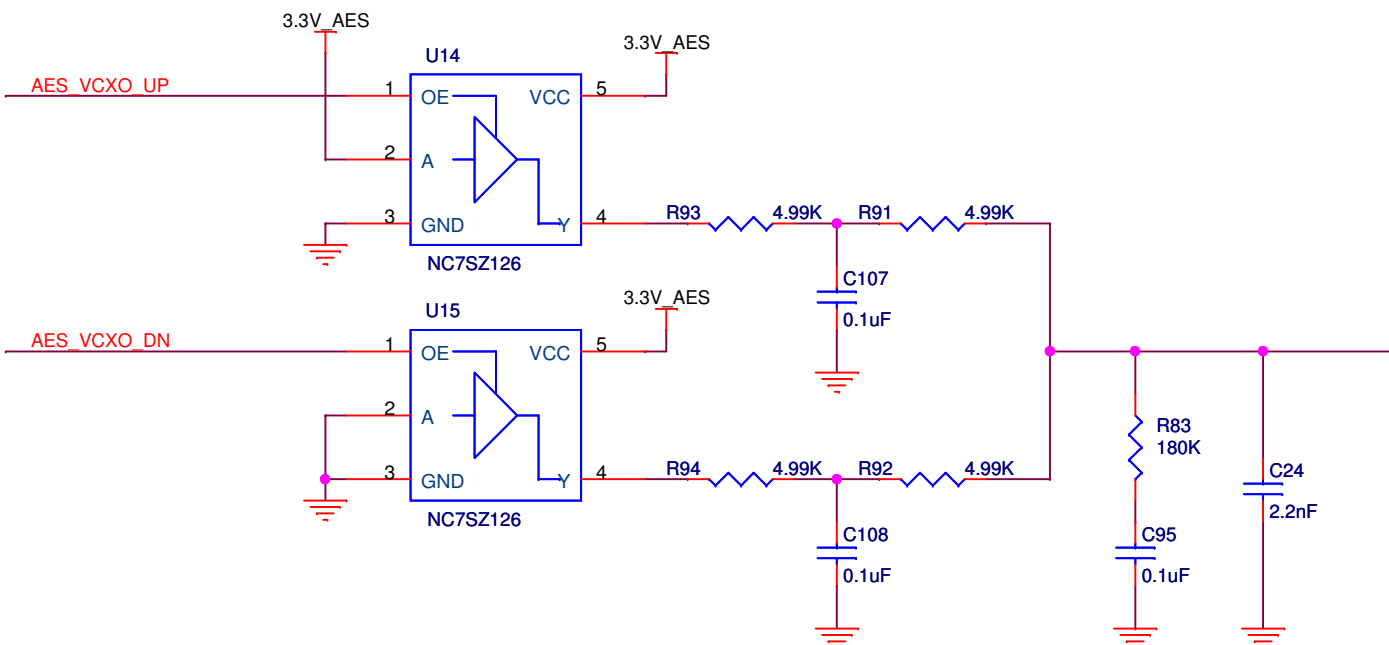
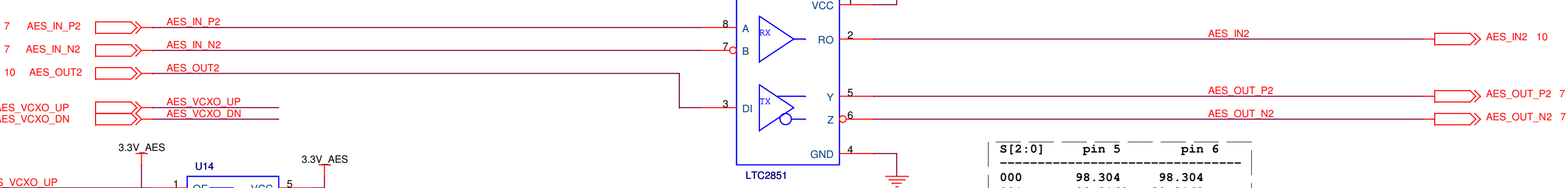
# AES3 TRANSCEIVER / CLOCK

AES3 INTERFACE (outputs)

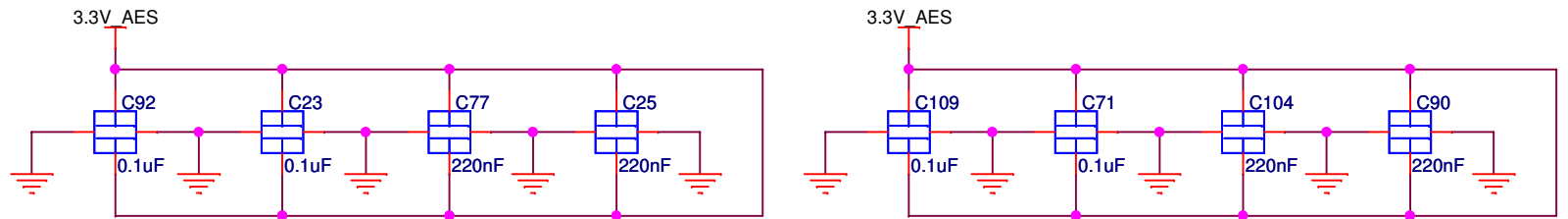
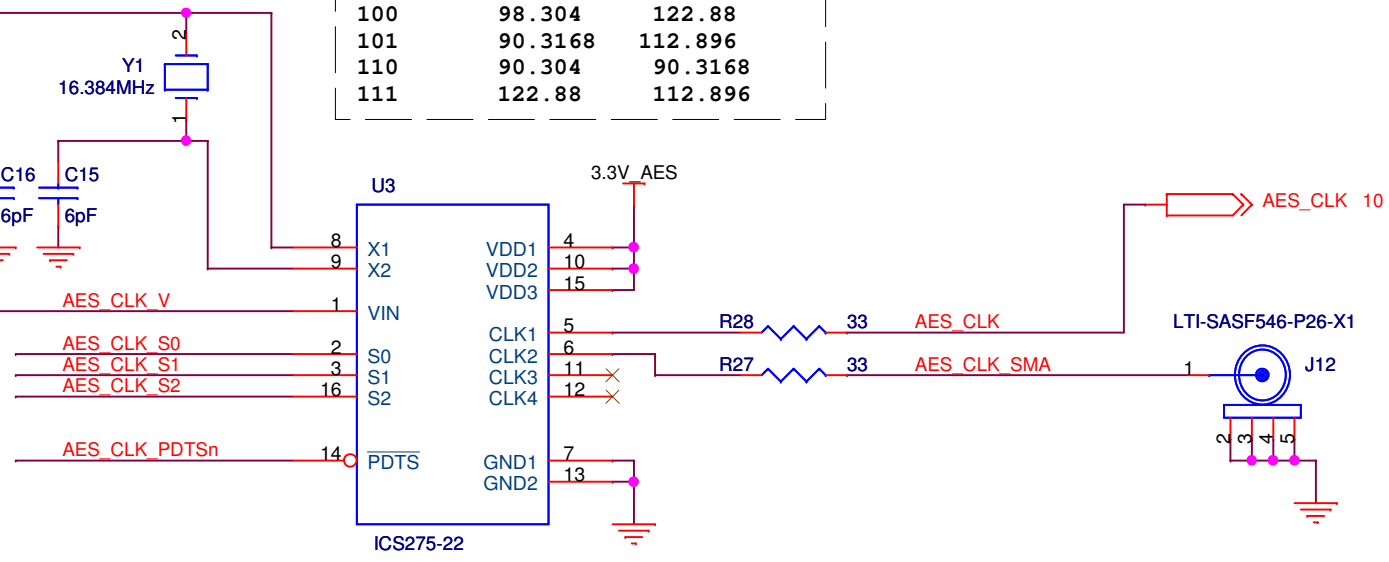
AES3 INTERFACE (inputs)



AES3 INTERFACE (inputs)

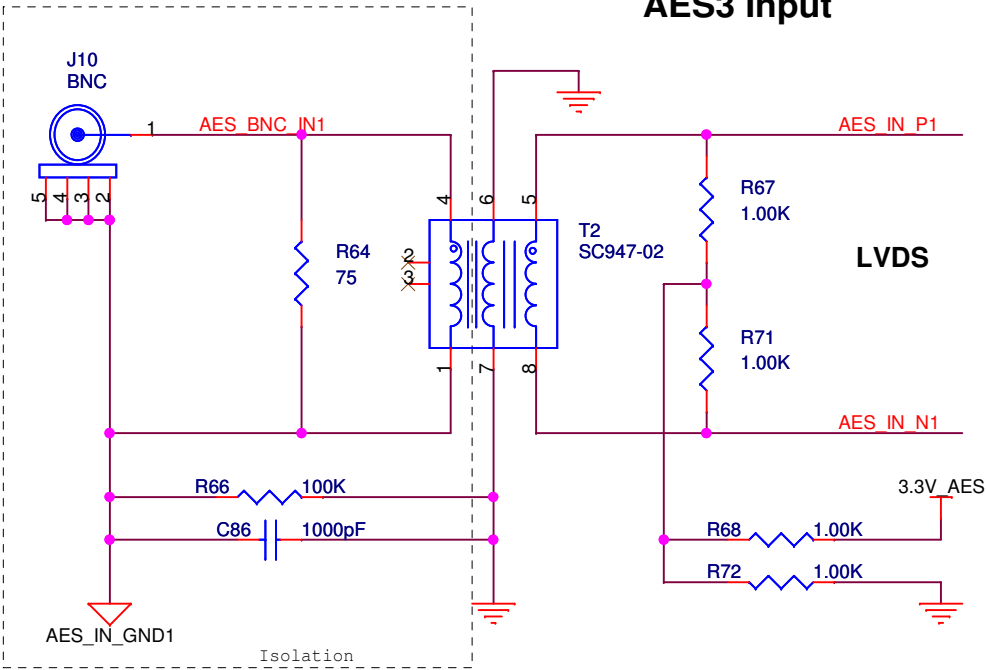


S[2:0]	pin 5	pin 6
000	98.304	98.304
001	90.3168	90.3168
010	122.88	122.88
011	112.896	112.896
100	98.304	122.88
101	90.3168	112.896
110	90.304	90.3168
111	122.88	112.896

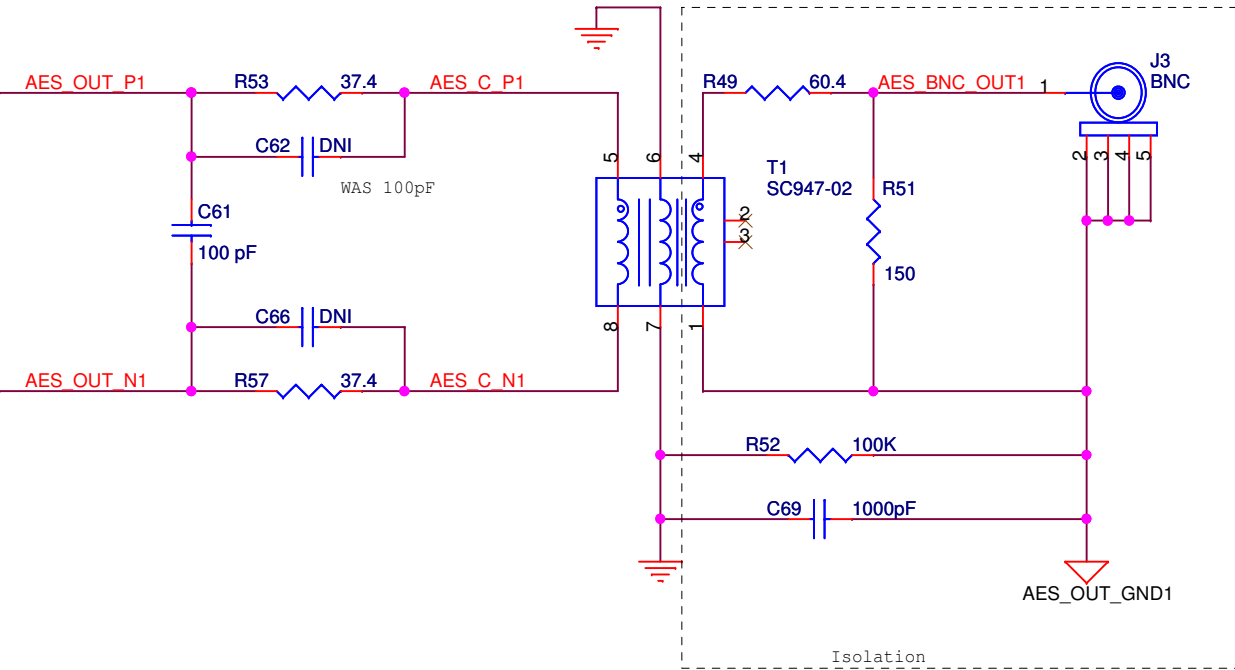


AES3 I/O

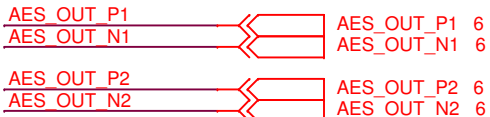
AES3 Input



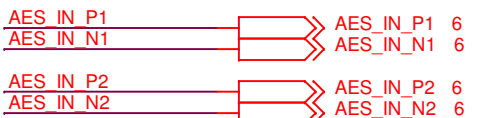
AES3 Output



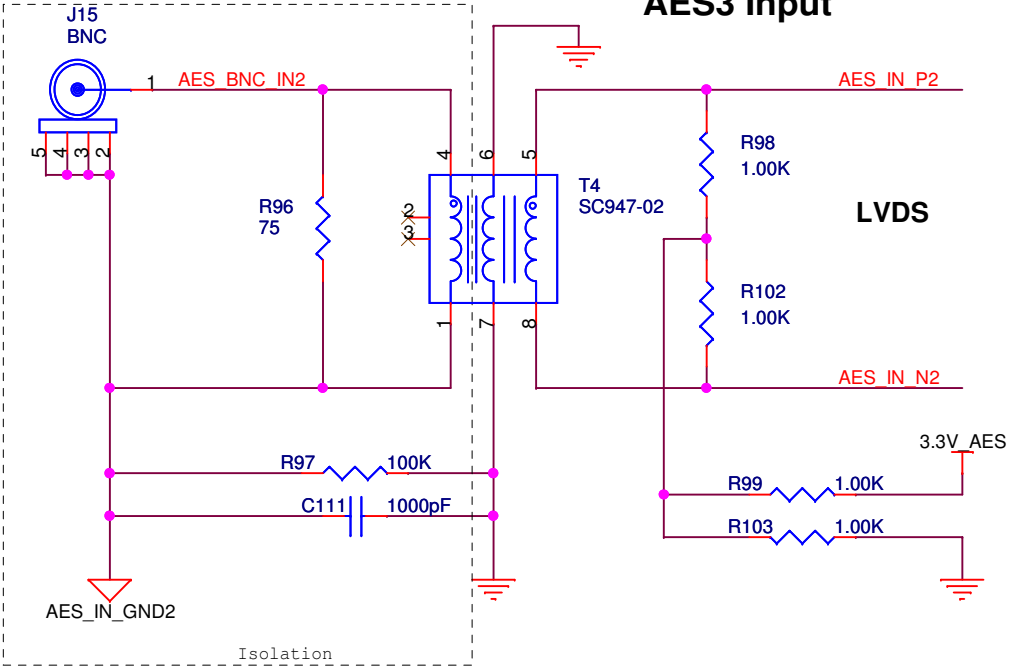
AES3 INTERFACE (outputs)



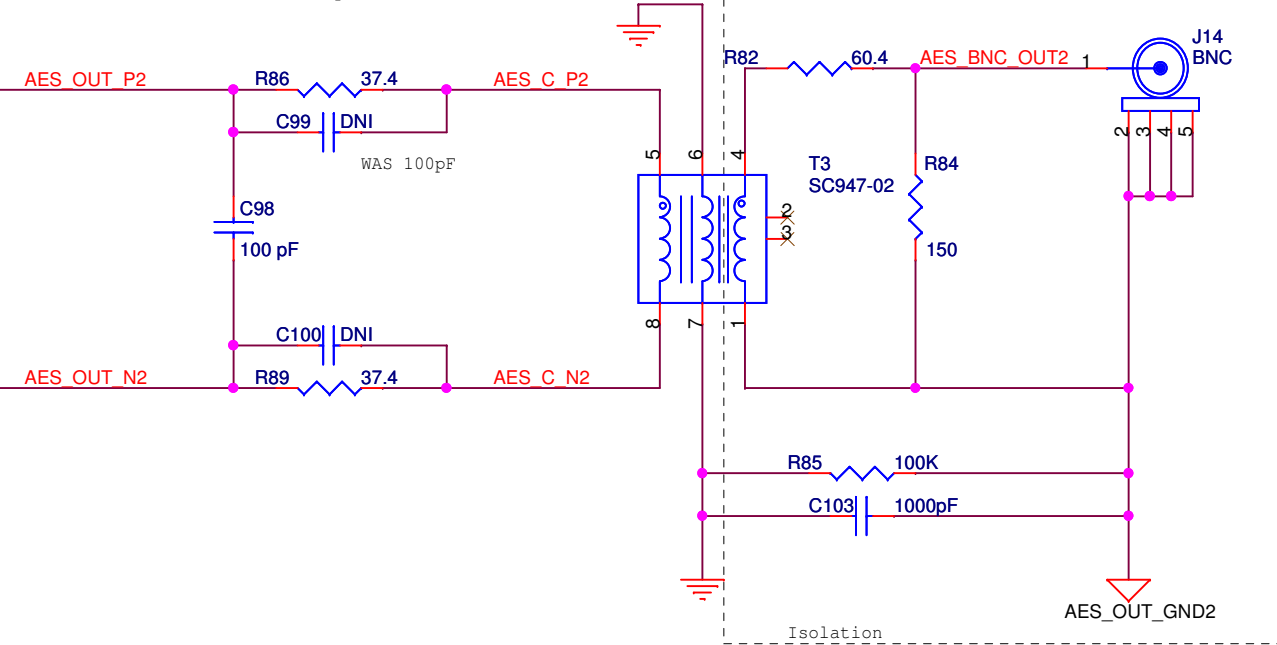
AES3 INTERFACE (inputs)



AES3 Input

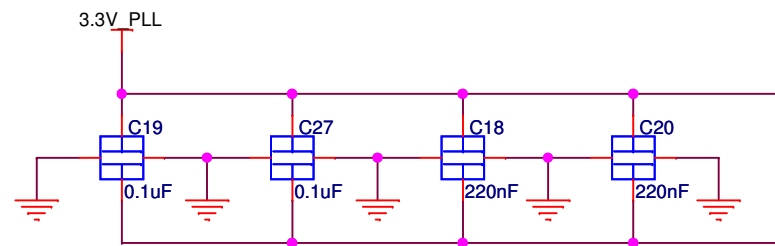
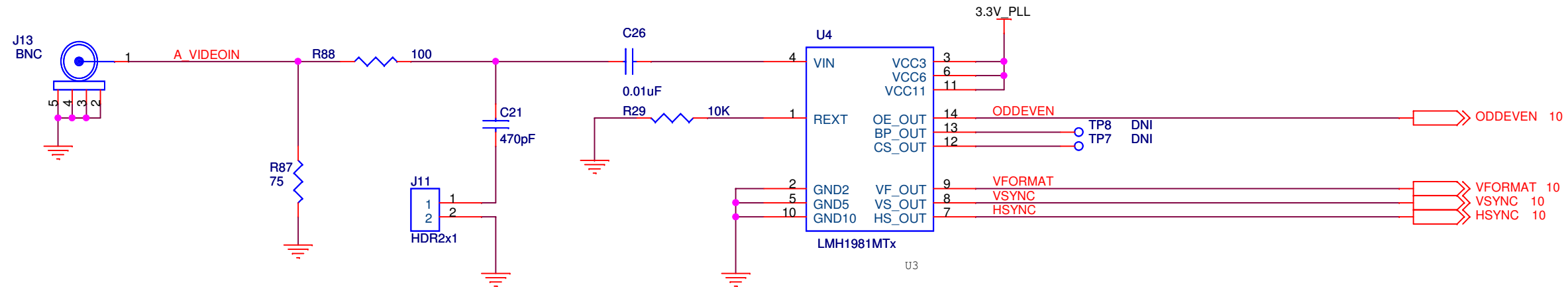


AES3 Output



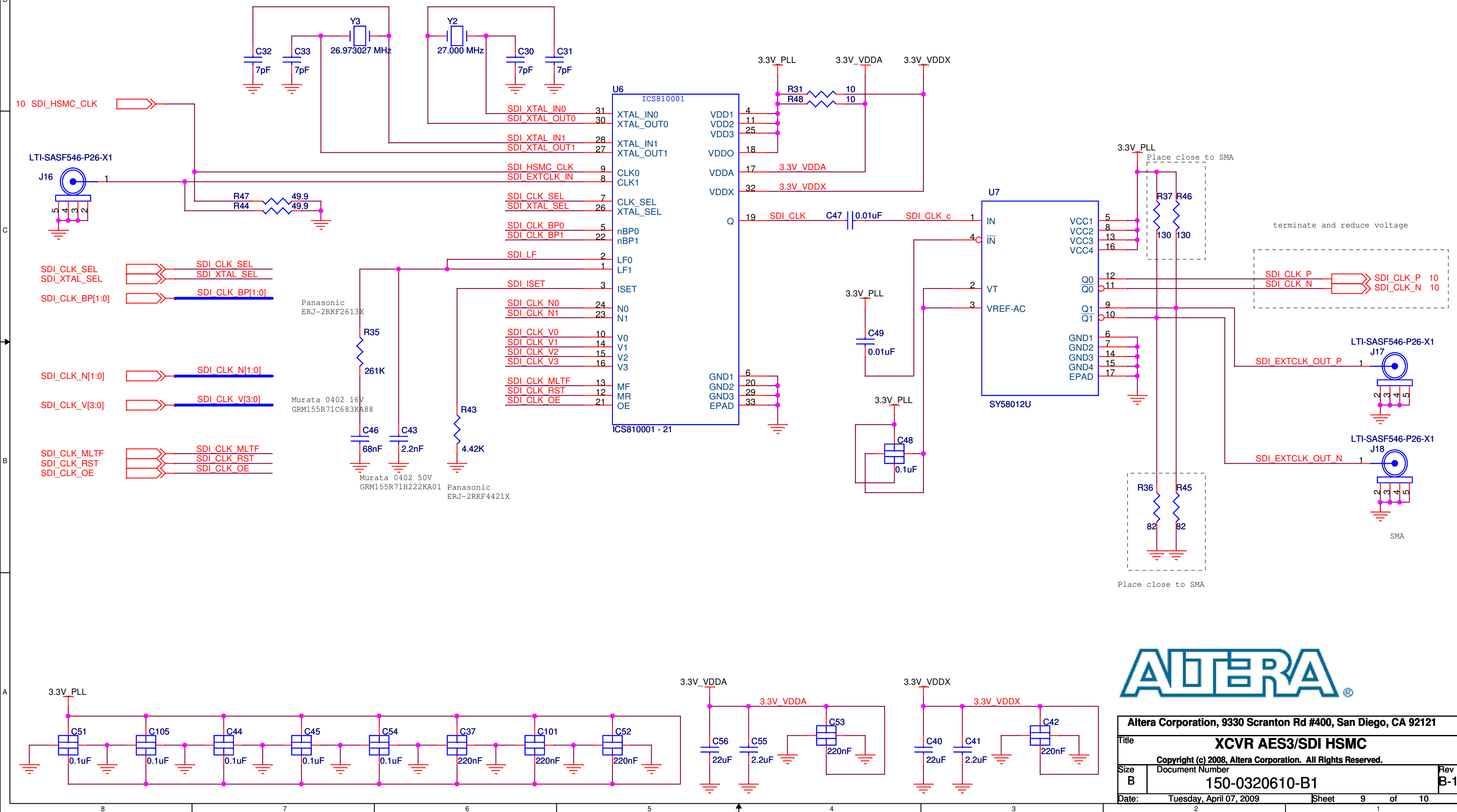
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# MULTI-FORMAT VIDEO SYNC SEPARATOR



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# SD / HD VIDEO CLOCK GENERATOR



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