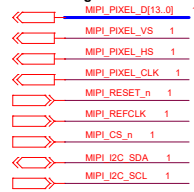
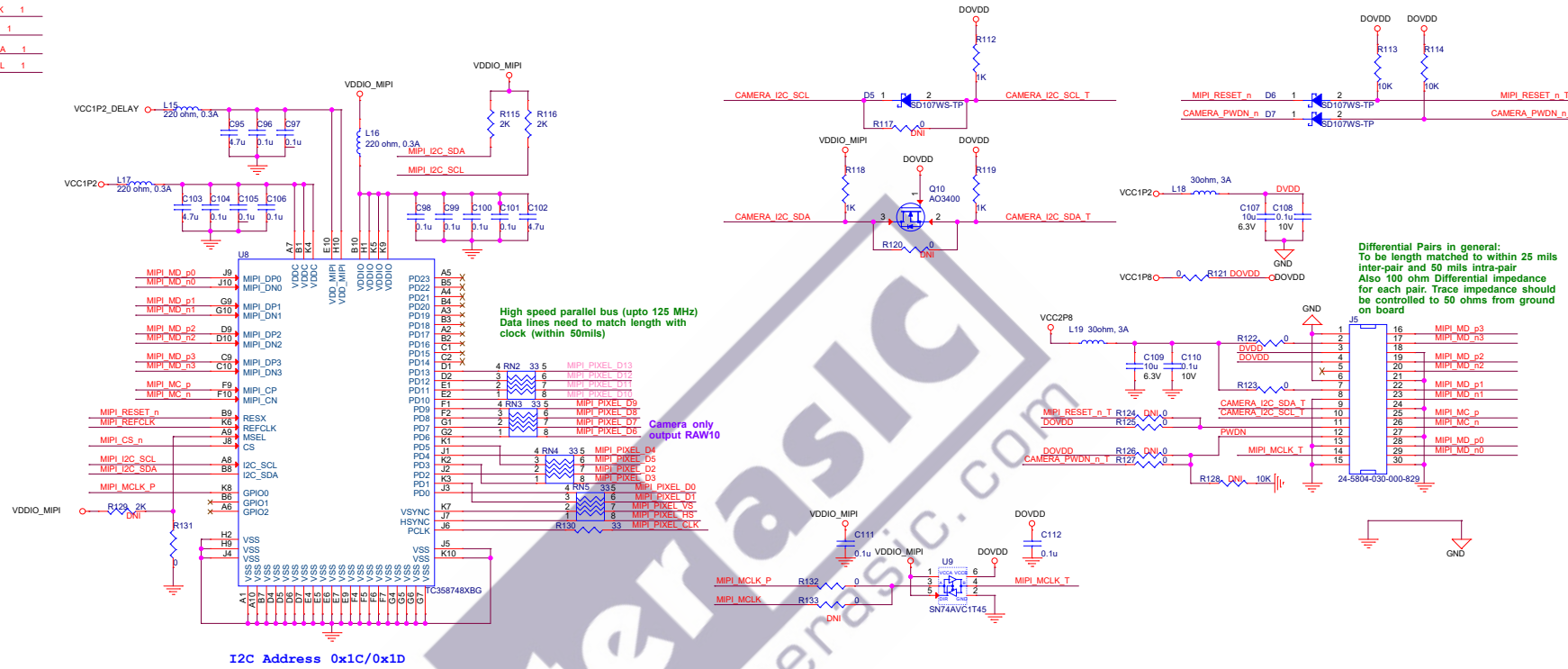


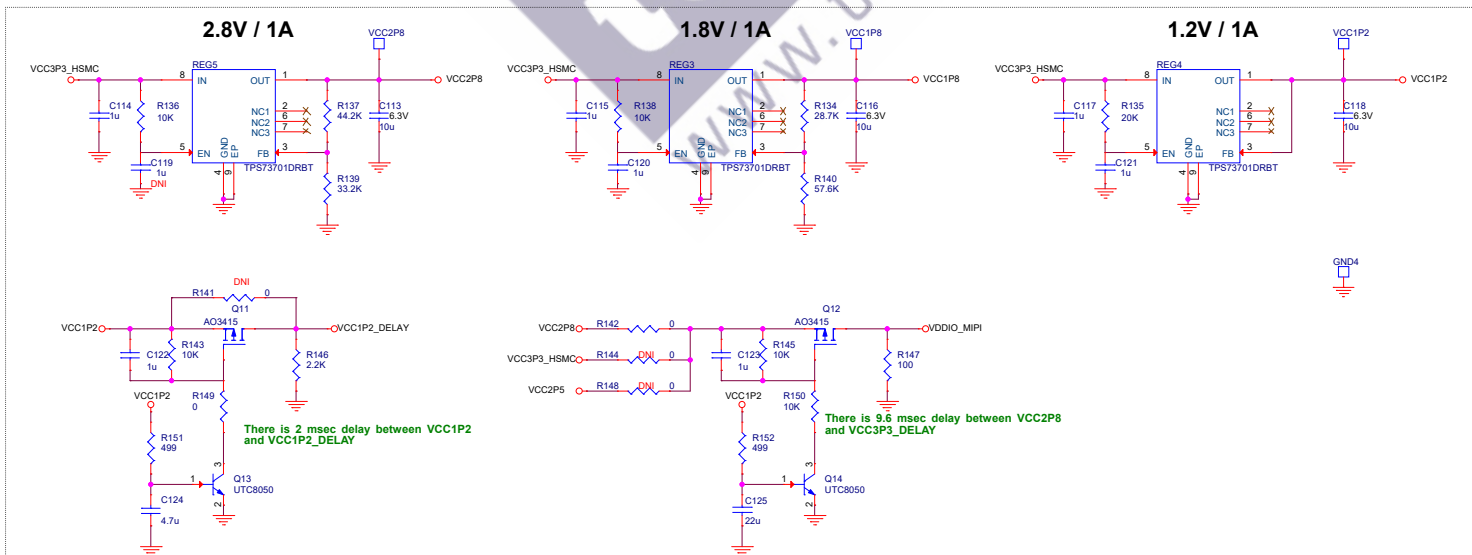
MIPI Camera



MIPI Camera Control Interface



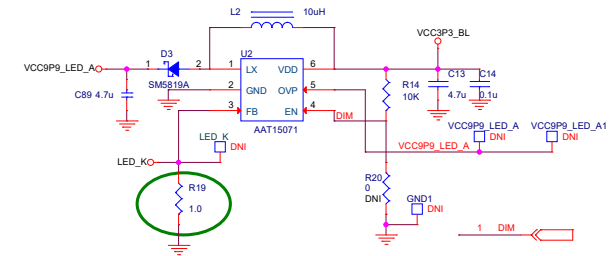
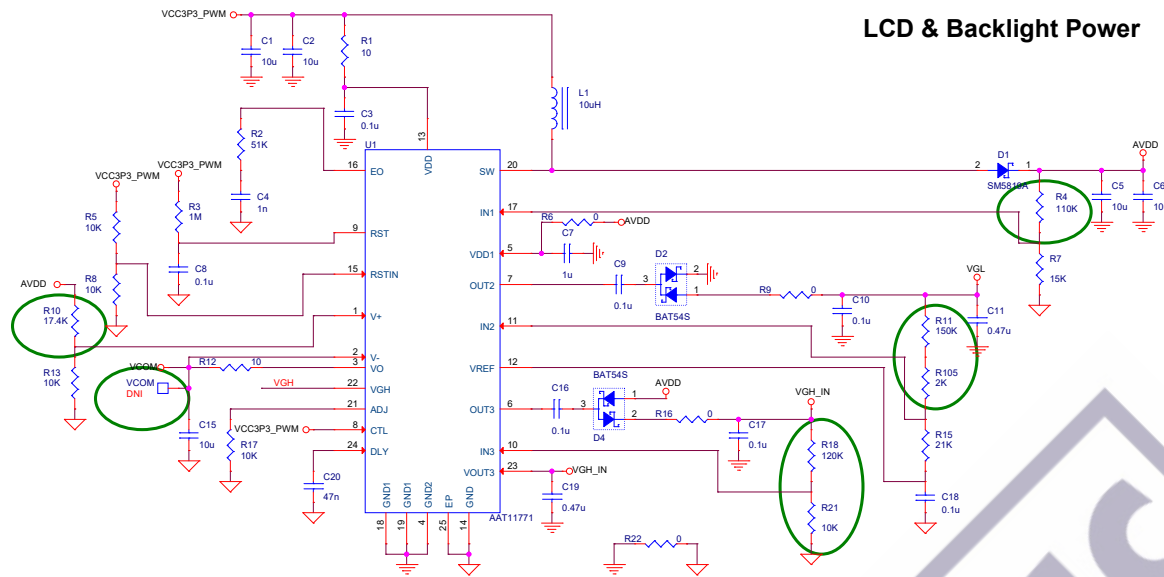
Differential Pairs in general:
To be length matched to within 25 mils
inter-pair and 50 mils intra-pair
Also 100 ohm Differential impedance
for each pair. Trace impedance should
be controlled to 50 ohms from ground
on board



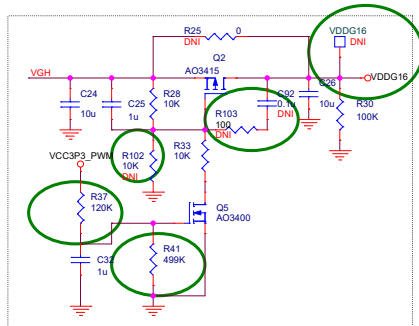
There is 2 msec delay between VCC1P2 and VCC1P2_DELAY

There is 9.6 msec delay between VCC2P8 and VCC3P3_DELAY

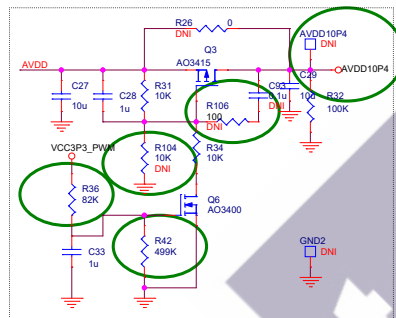
LCD & Backlight Power



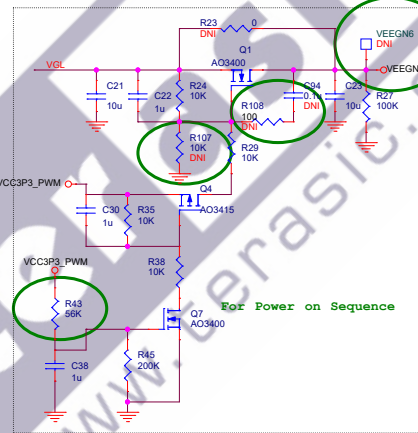
Power on sequence:
DVDD->VGL->AVDD->VGH->BL



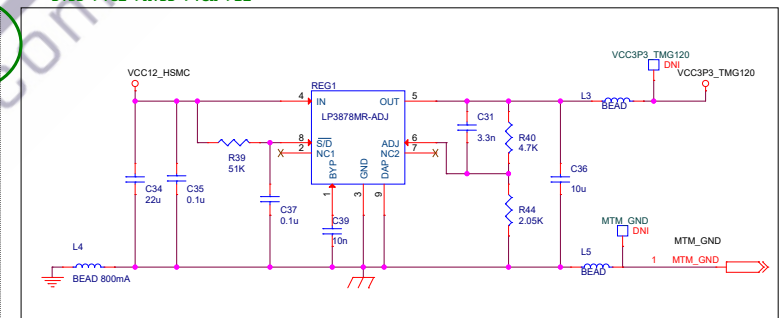
For Power on Sequence



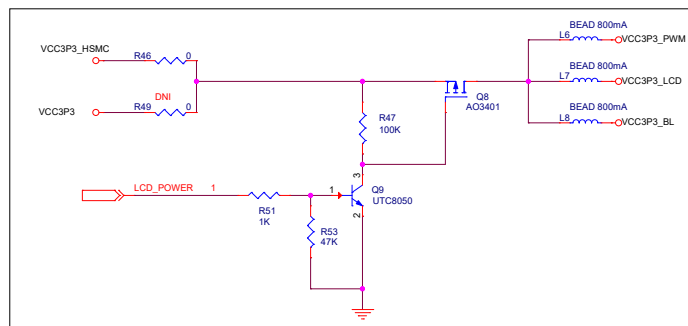
For Power on Sequence



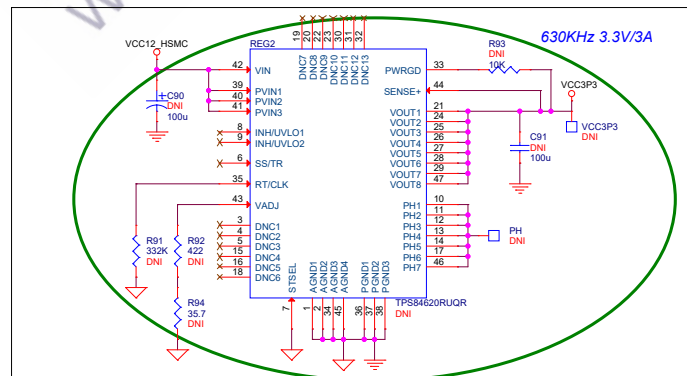
For Power on Sequence



power for MTM



LCD Power-on Switch (Default LCD Power-off)



Reserved power for LCD

